

New Approaches to Nanofabrication: Molding, Printing, and Other Techniques

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1. Introduction

“Nanofabrication” is the process of making functional structures with arbitrary patterns having minimum dimensions currently defined (more-or-less arbitrarily) to be ≤ 100 nm. Microelectronic devices and information technologies have improved and will continue to improve as a result of large-scale, commercial implementation of nanofabrication. The motivation for these improvements is to increase the density of components, lower their cost, and increase their performance per device and per integrated circuit.

The smallest physical gate length of a microprocessor currently in production is 37 nm,¹ and current half-pitch, or periodicity, of manufactured dynamic random-access memory (DRAM) is 90 nm. The International Technology Roadmap for Semiconductors (ITRS), published by the Semiconductor Industry Association (SIA), projects reaching the 45-nm node in 2010 (corresponding to transistor gate lengths down to 18 nm and DRAM spacing of 45 nm).¹ It is likely that a number of new technologies will evolve with further developments in nanofabrication.

Many materials with minimum dimensions on the nanoscale have properties different than those observed for the bulk material. For example, quantum

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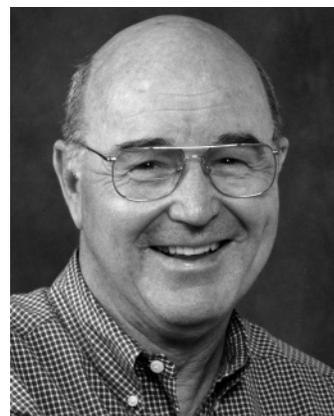
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dots can exhibit single-electron tunneling,²⁻⁵ carbon nanotubes can have high electrical conductivity and mechanical strength,⁶⁻¹² and thin polymer films can have glass-transition temperatures higher or lower than thick films.¹³⁻¹⁶ There is an expectation that

new technologies will emerge from the fabrication of nanostructures and nanostructured materials and also that nanofabrication will have new applications beyond information processing and storage in areas such as optics, biomedicine, and materials science.^{17–19}

Methods used to generate nanoscale structures and nanostructured materials are commonly characterized as “top-down” and “bottom-up”. The top-down approach uses various methods of lithography to pattern nanoscale structures. This approach includes serial and parallel techniques for patterning features—typically in two-dimensions (2D)—over length scales approximately 4 orders of magnitude larger (in linear dimension) than an individual structure. The bottom-up approach uses interactions between molecules or colloidal particles to assemble discrete nanoscale structures in two and three dimensions.

This manuscript first briefly reviews “conventional” techniques for nanofabrication; this review serves as background for discussions of “unconventional” techniques. These top-down techniques include photolithography and scanning beam (or maskless) lithography (e.g., electron beam and focused ion beam lithography). The limitations of these conventional approaches—such as their high capital and operating costs, the difficulty in accessing the facilities necessary to use them, and their restricted applicability to many important classes of problems—motivate the exploration and development of new, or “unconventional”, nanofabrication techniques. These unconventional approaches, of course, have limitations of their own.

Unconventional nanofabrication—the focus of the review—includes both top-down and bottom-up approaches. We discuss advances in unconventional nanofabrication within the areas of molding,^{20,21} embossing,^{22,23} printing,^{24–27} scanning probe lithography (SPL),^{28–34} edge lithography,^{35–43} and self-assembly.^{44–47} The first three techniques are primarily top-down approaches to nanofabrication. Scanning probe lithography and self-assembly, however, bridge top-down and bottom-up strategies for nanofabrication; these two techniques often use templates fabricated by top-down methods to direct the bottom-up assembly of components.^{48–50}

“Conventional” and “unconventional” techniques are at different stages of development. Conventional techniques for nanofabrication are commercially available and widely implemented in manufacturing. Conventional techniques have relatively high cost and low throughput; they are also largely restricted to planar fabrication in semiconductor materials and are incompatible with many problems in nonstandard fabrication (e.g., fabrication on nonplanar substrates, large area and low-cost fabrication, and three-dimensional fabrication). Conventional fabrication techniques also expose substrates to corrosive etchants, high-energy radiation, and relatively high temperatures. Alternative techniques are necessary when patterning relatively fragile materials, such as organic materials (especially biological materials) other than photoresist.

Unconventional routes to nanofabrication are often followed in research; they may also offer alternatives

to photolithography in manufacturing. Tools for molding on the nanometer scale are just entering commercial production. These techniques create opportunities for fabrication on nonplanar surfaces (particularly smooth, curved surfaces) and over large areas and may offer competition in nanofabrication where cost and materials make photolithography difficult. They are probably the only techniques that are applicable to biological materials and to sensitive organic and organometallic materials. The ability of any technique—conventional or unconventional—to prototype nanoscale structures rapidly and inexpensively will be a factor that influences the acceptance of that technique. Unconventional techniques have the potential to be the ultimate, low-cost method for nanomanufacturing; approaches based on reel-to-reel processing are particularly important for low-cost processes. Unconventional approaches are also operationally much simpler to use than are conventional techniques and thus help to open nanoscience and nanotechnology to exploration by a wide range of disciplines, especially those historically weakly connected to electrical engineering and applied physics.

We focus this review on research published in nanofabrication during the interval from 1999 to mid-2004 and to specific techniques demonstrated to be capable of patterning a substrate with features ≤ 100 nm in both lateral and vertical dimensions. In general, this definition excludes techniques with control over nanoscale features in only one direction, such as the deposition of thin films. Inorganic thin films, self-assembled monolayers (SAMs),⁵¹ or Langmuir–Blodgett (LB) films offer precise control over a structure only in the vertical direction. These materials can, however, sometimes be used to fabricate two-dimensional structures with nanoscale lateral dimensions.

The first area of unconventional nanofabrication that we review is a set of techniques that uses organic materials to replicate nanoscale patterns or masters. These patterns are transferred into the materials by molding, embossing, or printing. The second area of unconventional nanofabrication that we review (albeit in less detail) is scanning probe lithography (SPL). Techniques based on SPL are serial but can pattern features on a surface with atomic resolution.^{28,52} We also sketch advances in the development of a parallel approach to SPL. Our review of SPL is brief as this technique has been reviewed elsewhere.^{32,34,53,54} The last two areas that we review—edge lithography and self-assembly—are more limited than conventional lithography in generating arbitrary patterns but are promising approaches to low-cost, regular arrays of nanostructures. We believe that these approaches will be useful in research laboratories wishing to explore ideas in nanoscience. There are many forms of edge lithography; generally, they are techniques in which the *edges* of one pattern become the *features* of a second pattern. One approach to edge lithography converts films that are thin in the vertical direction into structures that are thin in the lateral direction. A second approach to edge lithography transfers the edges of a patterned thin film into another material. Self-assembly (both templated and untemplated) offers a final set of new

Table 1. Capabilities of Conventional and Unconventional Nanofabrication Techniques

technique	current capabilities (2004)		
	minimum feature ^a	resolution	pattern
photolithography ^{1,b}	37 nm	90 nm	parallel generation of arbitrary patterns
scanning beam lithography ^{88,c}	5 nm	20 nm	serial writing of arbitrary patterns
molding, embossing, and printing ^{116,123,168,d}	~5 nm	30 nm	parallel formation of arbitrary patterns
scanning probe lithography ^{28,52}	<1 nm	1 nm	serial positioning of atoms in arbitrary patterns
edge lithography ^{39,e}	8 nm	16 nm	parallel generation of noncrossing features
self-assembly ^{353–357,f}	>1 nm	>1 nm	parallel assembly of regular, repeating structures

^a Refers to the minimum demonstrated lateral dimension. ^b A resolution (pitch) of 45 nm is projected for 2010 using 157-nm light, soft X-rays, or optical “tricks” (e.g., immersion optics). ^c Obtained with a focused ion beam. Limited by photoresist sensitivity and beam intensity. ^d Limited by available masters and, ultimately, van der Waals interactions. ^e Potentially smaller sizes could be obtained using atomic layer deposition. ^f Self-assembly produces structures with critical feature sizes from 1 to 100 nm or larger.

approaches to nanofabrication. We believe that templated self-assembly will be very important in nanoscience but is early in its development.

Table 1 compares the current capabilities of conventional and unconventional methods for patterning nanostructures. This table summarizes the current minimum feature size (minimum lateral dimension), the highest resolution (pitch), and the types of patterns that can be generated reproducibly by each technique. The sections of the review that follow also discuss the current applications and limitations of these techniques as well as areas that may lead to further advances in nanofabrication.

2. Conventional Techniques for Nanofabrication

2.1. Overview

The microelectronics industry has developed a sophisticated infrastructure for patterning nanoscale features by conventional lithography. There are two dominant methods for conventional lithography: photolithography and particle beam lithography. Photolithography is the method of choice for manufacturing in the microelectronics industry. The most advanced photolithographic systems project collimated light through a quartz plate that supports a patterned chromium coating. The chromium mask has openings with linear dimensions approximately four times larger than the final image projected onto a photoresist located at the focal plane. This projection lithography can expose an ~8 cm² area of photoresist coated on a planar substrate—typically a semiconductor wafer—in a few seconds. A photoresist is an organic material that cross-links and becomes insoluble or that changes chemically and becomes more soluble in a basic solution upon exposure to high-energy short-wavelength light (e.g., UV light).^{55–57}

The exposed photoresist is immersed in solvents that dissolve the exposed (positive photoresist) or unexposed (negative photoresist) regions and provide patterned access to the surface of the substrate. The patterned photoresist masks the substrate during a subsequent step that chemically modifies the exposed regions of the substrate (Figure 1). A modern “step-and-scan” photolithography system can pattern over one hundred 300-mm diameter wafers per hour with 65-nm resolution; it can also cost tens of millions of dollars.⁵⁸

Scanning beam lithography is a serial process most often used to produce photomasks for projection

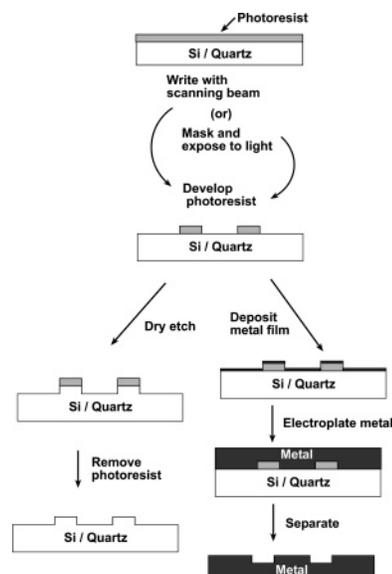


Figure 1. Schematic illustration of the fabrication of topographically patterned surfaces in hard materials by conventional photolithography and electroplating.

lithography rather than for actual device fabrication. Writing time for scanning beam lithography depends on pattern density and feature size. Patterning dense arrays of sub-20-nm features over an area of ~1 cm² requires ~24 h; this rate of patterning restricts scanning beam lithography techniques to small areas or low densities of features.

2.2. Photolithography

In current semiconductor nanofabrication photolithography can pattern 37-nm wide features with 193-nm wavelength light. The microelectronics industry plans to pattern minimum features below 37 nm using photolithography.¹ Continuing this trend with 193-nm light will require optical proximity correction (OPC) or phase-shifting mask technology, which significantly increases the cost of photomasks.

Another potential route to features with sub-50-nm resolution using 193-nm light is “immersion lithography”.^{59–64} Immersion lithography is analogous to the better known concept of immersion microscopy often used with biological specimens.⁶⁵ Imaging resolution for immersion microscopy is improved by increasing the refractive index of the medium between the imaging lens and the imaging

plane.⁶⁵ Increasing the index of the fluid between the final lens element and the imaging plane improves depth of focus while also allowing lenses with larger capture angles (numerical aperture) to be used in the imaging system. Switching the immersion fluid from air ($n = 1$) to water ($n = 1.47$ at 193 nm) allows the lens systems to be designed with numerical apertures approaching 1.3, thus significantly improving ultimate achievable imaging resolution.⁶⁶ The technical challenges of contacting the photoresist-coated substrate and mask with water (or another solvent) and fabricating complex, aspheric, catadioptric (both reflective and refractive) lenses must be solved before immersion lithography can be implemented, but this technology offers a potential route to high-volume production of devices with sub-50-nm resolution.

To pattern still smaller features, photolithography will require further advances, such as decreasing the imaging wavelength to 157 nm^{67,68} or to soft X-rays (~ 13.5 nm)—known in the microelectronics industry as extreme ultraviolet (EUV) light.^{69,70} The shift to shorter wavelengths of light requires new photoresists to alter the wavelength sensitivity and resolution of the resist^{70–73} as well as new light sources and, especially, new types of optics based on reflection rather than transmission to focus the light.^{74–77}

Photolithography has a number of advantages over scanning beam lithography in nanofabrication, but the time and cost required to fabricate the photomask—typically patterned by scanning beam lithography—can be a significant drawback. There is, however, one photolithographic method that can produce simple patterns (e.g., diffraction gratings) without using a photomask. This process is interferometric lithography,^{78,79} which involves the constructive and destructive interference of multiple laser beams at the surface of a photoresist. This method does not require a photomask or most of the expensive projection optics, but the projected patterns are restricted to regularly spaced arrays of lines or dots. Some of the smallest features—patterns of 40-nm wide parallel lines separated by 57 nm—produced by photolithography have, however, been generated using interferometric lithography.⁸⁰

2.3. Scanning Beam Lithography

Scanning beam lithography is a slow process relative to photolithography. This serial technique can, however, generate high-resolution features with arbitrary patterns. There are three main classes of scanning beam lithography: (i) scanned laser beams with ~ 250 -nm resolution are the least expensive; (ii) focused electron beams with sub-50-nm resolution (depending on tool settings and the choice of photoresist) are expensive to purchase and maintain; and (iii) focused ion beam (FIB) systems with sub-50-nm resolution are primarily (and extensively) used in research. Typically, high-resolution photomasks are patterned using laser writers and electron-beam tools.

There are tradeoffs for high-resolution patterning with an electron or ion beam. Increasing the resolution requires decreasing the diameter of the particle beam, which decreases the beam current (charge—

charge repulsion makes small, high-current beams unstable). These changes increase the time necessary to achieve the same imaging dose. Some improvements can be realized by using very sensitive resists. Resists that require a lower dose of electrons or ions, however, usually have lower resolution than photoresists that require a higher dose. A chemically amplified photoresist requires a low dose of electrons ($\sim 10 \mu\text{C}/\text{cm}^2$ for an accelerating potential of 100 kV) to pattern features with a resolution > 50 nm.⁸¹ A photoresist requiring a higher dose of electrons, such as poly(methyl methacrylate) (PMMA), however, can have resolution below 20 nm but requires a dose of $\sim 400 \mu\text{C}/\text{cm}^2$ (at 50 kV).⁸² A cold developer (< 10 °C) may improve the resolution and clarity of features in both types of resists.⁸³

A focused ion beam can write patterns into a photoresist or directly onto the substrate.⁸⁴ This technique can “mill” substrates by selectively removing material through ion bombardment or create patterns in an additive process by ion deposition or a localized chemical vapor deposition.^{85–87} This lithography technique can pattern features in a semiconductor with resolution down to ~ 20 nm and with the smallest lateral dimensions down to ~ 5 nm.⁸⁸ Common sources of contamination in FIB lithography are from implanted ions or material displaced from the substrate after milling. Implantation of ions can, however, be useful for transistor fabrication and repair, and the ability to write with different ions is potentially useful in tuning the properties of electronic nanostructures.

3. Nanofabrication by Molding and Embossing

A number of different procedures—molding, embossing, and printing—have been developed for patterning nanoscale structures. We divide molding and embossing techniques into two categories: (i) molding and embossing of nanostructures with a hard mold and (ii) molding and embossing of nanostructures with a soft (elastomeric) mold. Molding involves curing a precursor (usually a monomer or a prepolymer) against a topographically patterned substrate. This method of pattern transfer is used by techniques such as step-and-flash imprint lithography (SFIL),²⁰ replica molding (RM) with a soft mask,^{21,89} microtransfer molding (μTM),⁹⁰ and micromolding in capillaries (MIMIC).⁹¹ Embossing (or imprinting) techniques transfer a mold with a structured topography into an initially flat polymer film. These techniques include nanoimprint lithography (NIL)⁹² and solvent-assisted micromolding (SAMIM).⁹³

3.1. Hard Pattern Transfer Elements

Techniques such as relief printing and injection molding use hard molds or stamps to transfer a patterned topography into a monomer, prepolymer, or polymer substrate. Commercialized processes that use hard molds include patterning of compact discs (CDs), digital versatile discs (DVDs), diffraction gratings, holographic gratings (e.g., for identification markings on credit cards and currencies), and plastic parts.^{94–101} Hard molds can also transfer nanoscale

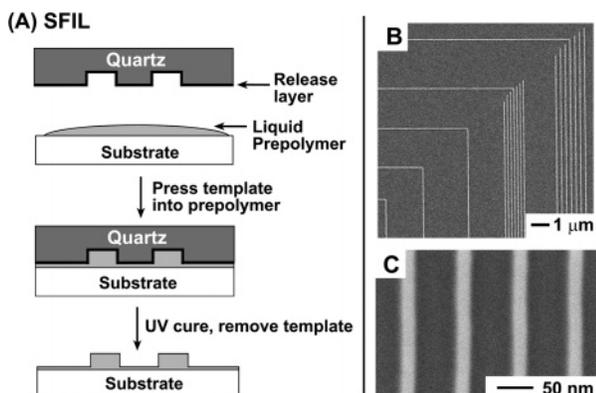


Figure 2. (A) Schematic illustration of the procedure for step-and-flash imprint lithography (SFIL).²⁰ Scanning electron microscopy (SEM) images of (B) 40-nm wide lines and (C) 20-nm wide lines patterned by step-and-flash lithography. The mold for the pattern in B was used for more than 1500 previous imprints. (Reprinted with permission from ref 20. Copyright 1999 SPIE.)

features into polymer films for nanofabrication. Nanostructured hard molds are prepared by transferring a structure patterned in photoresist into a hard substrate using reactive ion etching (RIE), wet chemical etching, or electroplating (Figure 1). Hard molds have been fabricated out of quartz, silicon, and metals.^{20,92,102–106} The most commonly used hard materials have been quartz and silicon. The smallest features transferred into a silicon mold are ~ 10 -nm wide lines written using electron-beam lithography.¹⁰⁷ The smallest features produced in a quartz mold are 20 nm.^{108–110}

A hard mold offers a number of advantages for nanofabrication. The rigid mold of silicon or quartz retains nanoscale features with minimal local deformation (the pressures required for embossing can cause long-range distortions in the substrate). A hard mold is thermally stable at temperatures used to cross-link most polymer precursors. Silicon and quartz molds are chemically inert to precursors used to mold polymers. Surface fouling of the pattern transfer element is, however, dependent on the surface free energies of the mold and the polymer, and a fluorosilane [e.g., $\text{CF}_3(\text{CF}_2)_6(\text{CH}_2)_2\text{SiCl}_3$] is usually covalently linked to the surface of a hard mold to facilitate the release of the mold from the polymer and reduce surface fouling. One of the main differences between silicon and quartz substrates is that quartz is transparent to ultraviolet and visible wavelengths of light and silicon is not. It is, therefore, possible to align the quartz mold optically to features on the underlying substrate and initiate photoinduced cross-linking of a molded prepolymer by exposure to UV light through the pattern transfer element. Step-and-flash imprint lithography exploits these properties of a quartz mold for nanofabrication.²⁰

3.1.1. Step-and-Flash Imprint Lithography (SFIL)

Step-and-flash imprint lithography is a technique that replicates the topography of a rigid mold using a photocurable prepolymer solution as the molded material.^{20,104,105,111–113} In SFIL (Figure 2A) a low-

viscosity, photocurable liquid or solution fills the void spaces of the quartz mold. The solution consists of a low-molecular-weight monomer and a photoinitiator. Exposing this solution to UV light polymerizes and hardens the precursor while in contact with the mold. Removing the mold leaves a topographically patterned (inverse) replica on the substrate.

Step-and-flash techniques avoid incomplete mold filling—a problem for embossing polymers with a rigid mold—by using a monomeric fluid with a low viscosity (< 5 cPs). This lithographic technique is also insensitive to the effects of pattern density reported for NIL.^{114,115} Hydrodynamic forces, however, prevent complete displacement of the fluid by the mold, even for low-viscosity precursors. The incomplete displacement of fluid leaves a residual layer of cured material between patterned features. The substrate and mold must be parallel and both must be rigorously flat to ensure that the residual layer is uniform in thickness over the entire imprinted area. This residual layer can be removed by etching (e.g., RIE).

An important consideration in all nanomolding techniques is the lifetime of the mold. A release layer reduces the surface free energy of the mold and minimizes adhesion of cross-linked polymer to the mold. If the release layer fails, the cured polymer can adhere to the mold and foul its surface or break its features. The first reported release layer was a fluorinated silane²⁰ with a lifetime of less than 100 patterned substrates. New surface treatments have been developed (but not described in detail) for patterning that claim to provide more than 1500 consecutive substrates.¹¹⁶

Figure 2B,C shows topographic patterns generated using the SFIL technique. The SFIL process patterns features down to at least 20 nm across a field size of 6.25 cm^2 per molding step.¹¹⁶ This process can pattern dielectric gates for the fabrication of a metal oxide semiconductor field-effect transistor (MOSFET)¹⁰⁴ and is compatible with semiconductor device manufacturing. Step-and-flash imprint lithography is also being developed to pattern curved surfaces and topographies with multiple depths in a single step.^{117,118}

Step-and-flash imprint lithography uses a rigid, transparent mold to print features at a constant temperature (~ 22 °C) with low applied pressures ($< 1 \text{ lb/in.}^2$). This combination of factors gives SFIL an inherent advantage in its potential for fine layer-to-layer alignment, which is necessary for multilayer device fabrication. Distortions caused by differential thermal expansion are not an issue since the mold and substrate are not heated. The low printing pressure allows imprinting on brittle substrates and reduces distortion caused by flexing of the mold or substrate. The alignment accuracy in SFIL has been reported as high as $\pm 10 \text{ nm}$ (3σ).¹¹⁹

The development of SFIL techniques has focused primarily on semiconductor nanofabrication. The successful implementation of SFIL to other applications will require the development of new photocurable monomers and appropriate economics for the application. The precursors currently available for molding do not include the functional materials

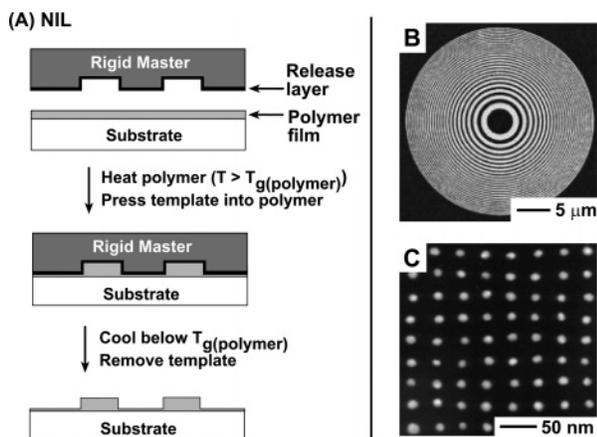


Figure 3. (A) Schematic illustration of nanoimprint lithography (NIL).^{22,131} The SEM images (B,C) show typical NIL experimental results. (B) Fresnel zone plates with a 125-nm minimum line width. (Reprinted with permission from ref 131. Copyright 2000 American Institute of Physics.) (C) Metal dots with a 10-nm diameter and a periodicity of 40 nm. (Reprinted with permission from ref 22. Copyright 1997 American Institute of Physics.)

required for optoelectronics and ferromagnetic coatings.

3.1.2. Nanoimprint Lithography (NIL)

Nanoimprint lithography^{23,103} refers to the pressure-induced transfer of a topographic pattern from a rigid mold (typically silicon) into a thermoplastic polymer film heated above its glass-transition temperature (Figure 3A). Another term for this method is “hot embossing” since the process involves heating the molded polymer above its glass-transition temperature. For example, to transfer the pattern from a mold into a thin polymer film of PMMA by NIL requires heating the polymer film above ~ 110 °C.

Nanoimprint lithography is a parallel plate printing process. Entire 100-mm wafers have been patterned in a single imprinting step.^{102,120} A second version of NIL uses “rolling molds” to emboss sheets with a repeating pattern.¹²¹ A third approach uses a modified commercial flip-chip bonder to imprint several fields across a substrate.¹²²

Nanoimprint lithography can mold a variety of polymeric materials (Figure 3B,C) and pattern features as small as ~ 5 nm^{22,123} and aspect ratios up to ~ 20 (height-to-width).¹²⁴ Materials that have been patterned successfully include biomolecules,¹²⁵ block copolymers,¹²⁶ conducting polymers,^{127,128} and fluorescently labeled polymers.¹²⁹ This process has been used to pattern components for a range of microelectronic, optical, and optoelectronic devices.^{130,131} For example, gate lengths in a MOSFET have been defined by NIL with a minimum feature size as small as 60 nm.¹⁰²

Nanoimprint lithography has made great progress in a relatively short period of time. One of the important issues still to be resolved is the useful lifetime of the mold. Presently, nanoimprint molds require replacement after ~ 50 consecutive imprints.¹³⁰ Heating and cooling cycles and high pressures (50–130 bar), applied during embossing, produce stress and wear on nanoimprint molds. This

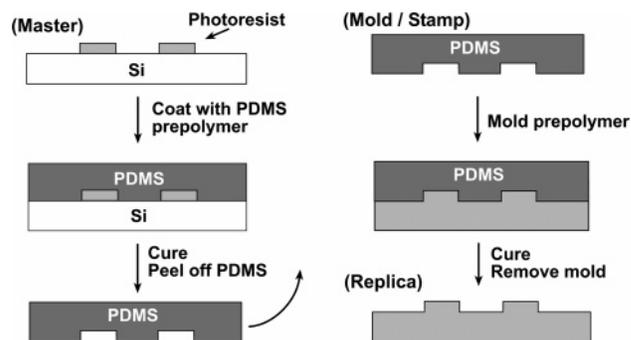


Figure 4. Schematic illustration of the formation of topographically patterned molds (or stamps, depending on the application) and replication of the master into a third functional material.

stress also presents a challenge for alignment during multilayer fabrication. Thermal cycling of the substrate also limits throughput to one imprint per 10 min. A laser-induced flash heating process may, however, reduce thermal cycling times.^{132,133} To prevent temperature cycling of the substrate, materials such as poly(dimethylglutarimide)¹³⁴ are imprinted at room temperature at an unstated pressure and polystyrene (PS) printed with pressures above ~ 300 atm.¹³⁵ Chou et al. imprinted patterns at room temperature using a transparent mold and UV-initiated cross-linking of the molded polymer.¹³⁶ The difference between UV-based NIL and SFIL is not distinct.

The high viscosity of the polymer films presents another challenge for nanofabrication using NIL. There appears to be an optimal pattern size and feature density for NIL.¹³⁷ Embossing micrometer-scale features can be more challenging than nanoscale features: filling large recesses within the mold requires more lateral displacement of the polymer than smaller recesses and thus increased process times (or higher temperatures and pressures). A mold containing a range of feature sizes may introduce distortions within the embossed film because of uneven displacement of the polymer and trapping of air bubbles. The thickness of the residual layer can also vary across the imprinted region depending on the pattern density or layout of the pattern. Nonuniformities in the residual layer present a challenge for transferring the pattern uniformly into the underlying substrate by RIE.

3.2. Soft Pattern Transfer Elements

Techniques that prepare a soft mold or stamp by casting a liquid polymer precursor against a topographically patterned master are commonly referred to as soft lithography (Figure 4).^{24,27} A number of polymers could be used for molding. Elastomers are a versatile class of polymers for replication of a topographic master. The most widely implemented and successful elastomer for nanofabrication is poly(dimethylsiloxane) (PDMS).^{24,27,54} Other elastomers tested as pattern transfer elements include polyurethane (PU), polyimide, and cross-linked Novolac resins (a phenol formaldehyde polymer).¹³⁸ Other siloxane elastomers are being developed for soft lithography, such as block copolymer thermoplas-

tics^{139,140} and fluorocarbon-modified siloxanes.¹⁴¹ A particularly interesting new class of polymers is highly fluorinated elastomers,^{142,143} which show excellent release properties and resistance to swelling by organic solvents and monomers. Molds from other polymers are also finding applications in nanofabrication, including polyolefins,¹⁴⁴ acrylate-based UV-curable polymers,¹⁴⁵ and an amorphous fluoropolymer.¹⁴⁶

Poly(dimethylsiloxane) has a number of useful properties for nanofabrication. This material is durable, unreactive toward most materials being patterned or molded, chemically resistant to many solvents,¹⁴⁷ and transparent above a wavelength of ~ 280 nm. Commercially available kits or precursors for this polymer can be obtained inexpensively.^{148,149} One of the major advantages of PDMS is that fabrication of molds or stamps (by replica molding) is so inexpensive that a large number of uses may not be necessary. In fact, sometimes the mold or stamp becomes a disposable reagent.^{150,151} This material can be deformed reversibly and repeatedly without permanent distortion or relaxation of the surface topography.²¹ The cured elastomer has a low surface free energy (21.6 dynes/cm²);¹⁵² this low surface free energy allows PDMS to be easily released after molding. Fluorosilane chemistry can be used to decrease the surface free energy to ~ 12 dynes/cm²—a value similar to poly(tetrafluoroethylene) or Teflon.^{142,153} Another method to reduce the noncovalent interactions of the mold–substrate interface during release is immersion in a solvent (e.g., methanol or hexane) or exposure to an organic vapor.^{154,155} Poly(dimethylsiloxane) is chemically inert, an advantage for patterning many different types of materials. These materials include polymers, precursors to carbons and ceramics, sol–gel materials, organic and inorganic salts, colloids, biological macromolecules, thiols, phosphonic acids, and silanes.^{24,27} An important limitation of PDMS is that it absorbs many nonpolar, low-molecular-weight organic compounds.¹⁴⁷ The swelling of the PDMS both compromises dimensional stability and leads to unwanted adhesion after polymerization of monomers. Other materials may circumvent this limitation.¹⁴²

The tensile modulus of 184-PDMS (Dow Corning) is relatively low (1.8 MPa) and limits the replication of nanoscale features.^{156–160} High-resolution masters are reproduced accurately in composite stamps of hard PDMS (h-PDMS)^{159,161–163} or UV-curable PDMS (hv-PDMS)¹⁶⁴ with tensile moduli of 8.2 and 3.4 MPa, respectively.

3.2.1. Replica Molding (RM)

Replica molding consists of three steps (Figure 4): (i) creating a topographically patterned master (usually by conventional techniques; see, for example, Figure 1); (ii) transferring the pattern of this master into PDMS by replica molding; and (iii) fabricating a replica of the original master by solidifying a liquid precursor against the PDMS mold.^{27,89,165} The replica can be cast from a photo- or thermally curable prepolymer. Nonpolar monomers and other low-

molecular-weight molecules often dissolve in PDMS.¹⁴⁷ Oligomers and prepolymers (and polar or highly fluorinated monomers), however, typically do not penetrate into PDMS. This low solubility extends the usefulness of each PDMS mold beyond 20 replications; ultimate limits to the number of cycles over which a mold can be used are not established. A benefit of molding with PDMS is the ability to mold against nonplanar, rigid, and soft topographic surfaces (unlike a hard mold, which requires a planar, rigid surface).

Replica molding can produce numerous molds, replicas, and patterned surfaces from each master and provide capabilities for nanofabrication not commonly available in an academic setting. Replication of the high-cost, high-resolution masters reduces the financial burden of patterning nanostructures and conveniently extends nanofabrication to a range of materials.

Replica molding has transferred ~ 30 -nm lateral features from a diffraction grating, a compressed PDMS stamp,^{21,89} and a nanoscale crystal into PU.¹⁶⁶ Air bubbles with sizes as small as ~ 20 nm were also replicated into PDMS.¹⁶⁷ A phase-separated block-copolymer film—with one component removed—trapped air bubbles in a regular array during molding with PDMS (Figure 5A–C). Features down to at least 1.5 nm were also replicated into PDMS from a regular pattern of vertical deflections.¹⁶³ Periodic patterns of rings (Figure 5D) or lines (Figure 5G) written into a PMMA film using an electron beam were molded into h-PDMS (Figure 5E,H) and replicated into PU (Figure 5F,I). Surface roughness of the PMMA, PDMS, and PU are each ~ 0.5 nm over an area of at least $1 \mu\text{m}^2$. The smallest features replicated using PDMS are ~ 3 -nm wide structures¹⁶⁸ and ~ 0.5 -nm vertical deflections.¹⁶⁹ In the later experiment the surface roughness is ~ 0.2 nm for each substrate over an area of $1 \mu\text{m}^2$. It is still not clear what the ultimate limit to replication using PDMS (or other materials) is, but the limit, based on the physics of van der Waals interactions, should be less than 0.5 nm.

Two techniques related to RM are micromolding in capillaries (MIMIC) and microtransfer molding (μTM). Micromolding in capillaries can fabricate isolated structures by using capillarity to fill channels in a PDMS mold with low-viscosity liquid solutions, such as a photo- or thermally curable prepolymer.⁹¹ Nanosized channels can be filled with liquids.^{170,171} These nanochannels are, however, slow (or impossible) to fill completely since resistance to flow in pressure-driven flow increases rapidly as the size of the channel decreases. The capillary flow can be assisted by applying a vacuum to one end of the capillaries, heating the liquid, or applying an electric field.^{172,173} Microtransfer molding also fabricates isolated structures but has been limited to a minimum feature size > 100 nm.^{90,174–176}

3.2.2. Solvent-Assisted Micromolding (SAMIM)

Solvent-assisted micromolding uses an elastomeric mold and an appropriate solvent to emboss polymer

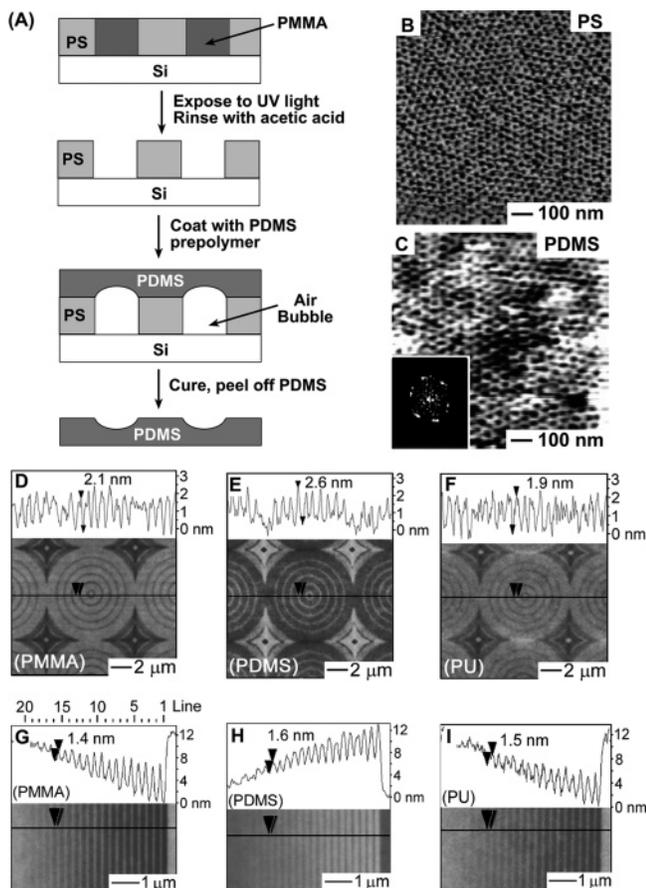


Figure 5. (A–C) Replica molding of air bubbles trapped within a porous polystyrene (PS) surface.¹⁶⁷ (Reprinted with permission from ref 167. Copyright 2003 Wiley-VCH Verlag GmbH.) (A) Schematic depiction of this replication. (B) Atomic force microscopy (AFM) image of the PS-*b*-PMMA copolymer master after selective removal of the poly(methyl methacrylate) (PMMA), and (C) a PDMS replica of air bubbles trapped within this master of ~ 20 -nm wide holes. The inset shows a two-dimensional Fourier transform of the PDMS replica. (D–I) Replica molding of nanoscale vertical deflections (~ 1 – 5 nm) patterned in PMMA by direct writing with a focused electron beam.¹⁶³ The series of AFM images include the (D,G) PMMA master, (E,H) h-PDMS mold, and (F,I) polyurethane (PU) replica.

films (Figure 6A).⁹³ This technique processes the polymer at ambient conditions with soft molds rather than at elevated temperatures with rigid molds. For SAMIM, a solvent swells or dissolves the polymer; swelling of the elastomeric mold is crucial to the process but limited by the amount of solvent contained in the polymer being molded and by the solubility and mobility (usually by diffusion) of the solvent in the PDMS. During solvent evaporation the softened polymer conforms to the surface of the PDMS mold. The gas and solvent permeability of the mold prevents nonuniform solvent evaporation and trapping of air bubbles at the interface. The precipitated polymer film retains an imprint of the surface topography from the mold.

Solvent-assisted micromolding has two characteristics useful in nanofabrication. It avoids cycling of the temperature of the sample and thus limits thermal oxidation or degradation of other system components. Elastomeric molds are also especially

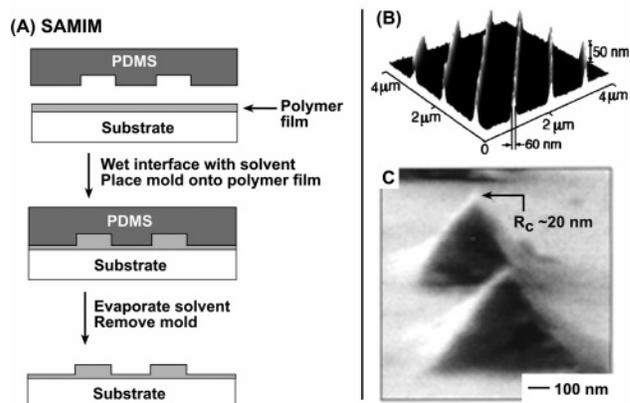


Figure 6. (A) Schematic illustration of solvent-assisted micromolding (SAMIM). (B) AFM image of 60-nm wide lines in a Novolac photoresist supported on a silicon substrate patterned using SAMIM.⁹³ Ethanol coated on the PDMS mold softened the photoresist. (Reprinted with permission from ref 93. Copyright 1997 Wiley-VCH Verlag GmbH.) (C) SEM image of square pyramidal structures showing a well-defined apex with an ~ 20 -nm radius of curvature (R_c) molded with a composite PDMS mold using SAMIM.¹⁵⁹

useful in embossing because the mold conforms to the polymer substrate—even when it is not planar—over areas > 1 cm² and allows uniform pattern transfer over that area. This process has been demonstrated for a number of polymers including Novolac photoresists, PS, PMMA, cellulose acetate, poly(vinyl chloride) (PVC), and the precursors to conjugated organic polymers.^{27,93,177–179} The mild processing conditions of SAMIM are compatible with patterning polymer-based distributed feedback lasers¹⁷⁸ and organic light-emitting diodes (OLEDs).¹⁷⁷

Although SAMIM makes it possible to mold polymers that are difficult to manipulate by SFIL and NIL, the in-plane dimensional stability of soft masks is believed (or simply *assumed*) to be lower than for hard masks. Whether this belief is justified by practice remains to be established. The application of SAMIM to patterning nanoscale features has been limited primarily by the lack of appropriate masters rather than by the fundamental characteristics of this process. Line widths as small as ~ 60 nm and aspect ratios of at least 1:1 have been patterned in a Novolac photoresist (Figure 6B) and poly(vinyl pyridine) by SAMIM.⁹³ A composite elastomeric mold with a h-PDMS surface has embossed features with minimum dimensions as small as ~ 20 nm (Figure 6C).¹⁵⁹

As with other embossing techniques, SAMIM using PDMS leaves a residue between isolated features (molding using fluorinated elastomers—with their lower interfacial free energies—may obviate this problem¹⁴²). The processing time for SAMIM depends on solvent transport through the PDMS. Swelling of the PDMS by the solvent can cause distortion of the topographic features. Understanding the interactions of solvents with PDMS, such as compatibility and swelling of the elastomer, will improve the capabilities of this technique.^{147,180}

3.3. Fundamental Limits of Molding and Embossing

The *resolution* of nanofabrication by molding and embossing is primarily limited by five factors: (i) the practicality of fabricating masters with small features; (ii) the ability of a material to mold, with high fidelity, the features of the master; (iii) the distortion of features in the transferred pattern; (iv) the swelling of the master by the monomers used or the solvent used to dissolve polymers; and (v) the ability of the molded material to fill the mold completely, the tendency of the system to trap bubbles of gas, the kinetics of filling of the mold, the thickness of the residue or “scum” layer (if any) between isolated features, and related issues concerning the mold, substrate, and polymer as a system. The *practicality* of nanofabrication by molding and embossing is limited by release of the mold from the polymer, cycle time, mold maintenance, and registration (or exact alignment) of features on the mold with structures on a substrate.

Advances in the capabilities of conventional lithographic tools will continue to decrease the smallest lateral dimensions in the masters. For example, direct-write electron-beam lithography can pattern a minimum feature size down to 5 nm.⁸² The ability to arrange and assemble small molecules or particles could also present a new strategy for fabricating masters with nanoscale features (further details are provided in section 7). The minimum feature size replicated by molding and embossing has, so far, decreased steadily with improvements in fabricating masters.

A number of factors limit the fidelity of replicating nanostructures by molding and embossing. The fidelity is, in principle, limited by the size of the molecular precursors of the polymer and the separation between the mold and master. A minimum separation of ~ 0.1 nm is predicted by van der Waals contacts, and < 0.5 nm is suggested by current experimental data.^{163,169,181} There is, thus, no fundamental physics-based limitation to molding in the nanometer range, although the actual limit to replica molding remains to be determined experimentally. The size and shape of the molded species—that is, molecules making up the material used in the polymer—and the polymer (and filler) making up the mold also limits the fidelity of pattern transfer. The granularity of matter at the atomic and molecular scale (0.1–1 nm) thus provides a chemical limit to resolution. As a consequence, small molecules such as monomers and prepolymers and amorphous materials such as silica gels may give better resolution for molding than higher molecular weight precursors (i.e., polymers).

The distortion of nanoscale features in the master, the mold, or the replica product limits the number of materials that can be used in molding and embossing for nanofabrication. One cause for distortion of replicated features is a difference in thermal expansion between the materials used to fabricate the master, mold, and replica. A second cause for distortion, which influences the fidelity of replication, is polymer shrinkage upon curing a precursor to a

polymer due to solvent evaporation and/or cross-linking during polymerization. A third cause for distortion is collapse or deformation of the molded features (or features on the mold) due to mechanical instability of the polymer (e.g., flexibility and polymer chain mobility): capillary and van der Waals interactions can be very important at small scales. A fourth cause for distortion is the forces required to overcome adhesion between the master and mold.¹⁸² One approach to reducing the adhesion between a polymer mold and a silicon or quartz master is to reduce the surface free energy of the master by modifying its surface with a fluorosilane. A second approach is to carry out the separation between mold and molded material in the presence of a vapor or liquid with very low viscosity, such as methanol.^{154,155} Further work is necessary to develop surface release layers for high-resolution masters.

Elastomeric molds are susceptible to distortion because of their low elastic modulus and high thermal expansion coefficient. Hard molds can also experience distortion even with relatively minor pressure differentials.^{115,183} The alignment of patterns with nanoscale precision is challenging for two reasons: (i) distortion of the mold and (ii) the low contrast in refractive index between the master (e.g., PDMS or quartz) and polymer precursor. These limitations often restrict molding and embossing techniques to fabricating structures with one or at most two layers. Quartz molds have been aligned with an accuracy as small as ± 10 nm (3σ);¹¹⁹ similar values will ultimately be achievable with other hard (and perhaps soft) molds.

3.4. Summary

Molding and embossing are the most widely pursued and successful techniques for unconventional nanofabrication. There are a number of molding and embossing techniques that can pattern, in parallel, nanometer-scale features over large areas (i.e., entire silicon wafers). These techniques have been used to pattern functional structures for inorganic- and organic-based microelectronics and optics.^{25,104,184} The formation of these structures requires a high-resolution master, typically generated by conventional nanofabrication techniques. These masters can, however, be inexpensively replicated by molding or embossing. The replication process can be repeated a number of times for a range of organic materials. The number of replications is limited by surface fouling, which is related to interfacial free energies. Another limitation is the variation in thickness of the residual layer (the so-called “scum layer”) between nanoscale features. It is challenging to transfer the pattern uniformly into an underlying substrate by RIE because of this variation in thickness. The number of replicas, surface area of each replica, and range of materials that can be patterned by molding and embossing have steadily increased, while the resolution and minimum feature size have steadily decreased.

4. Nanofabrication by Printing

4.1. Introduction

Transferring a material onto a substrate by printing from a topographically patterned stamp is also useful in nanofabrication. A process known as microcontact printing (μ CP) transfers molecules from a patterned PDMS stamp to a substrate by the formation of covalent bonds.^{138,185–191} In this process a solution of molecules (e.g., 5 mM alkanethiol in ethanol) is inked onto the surface of the PDMS, and this stamp is brought into contact with an appropriate substrate (e.g., a thin gold, silver, or palladium film). The molecules are transferred in a pattern defined by the topography of the stamp (i.e., the regions of atomic-level contact between the stamp and substrate) with a minimum feature size as small as ~ 30 nm.^{138,192–196} The flexibility of the PDMS stamp and the ability to achieve conformal, atomic-level contact between the stamp and the substrate are both advantageous for printing over large areas (>50 cm²)^{197,198} and on curved surfaces.^{199–201} This patterning technique has been developed primarily for PDMS stamps, although recent demonstrations have explored surface modifications of these stamps and other soft materials.^{202–205} Further details concerning μ CP of SAMs on metal films are covered in another article of this issue.⁵¹

4.2. Extensions of Microcontact Printing

Microcontact printing has patterned a number of materials other than self-assembled monolayers (SAMs), such as biomolecules,^{206–213} colloidal particles,^{202,214–217} and polymers.^{218–220} In this section we focus on more recent extensions of μ CP for patterning nanostructures. These extensions include patterning of surfaces by electrical contact (electrical μ CP, or e - μ CP)²²¹ and release of material coatings from a patterned stamp (nanotransfer printing, or nTP).²²² Both techniques use a PDMS stamp or a PDMS substrate to achieve compliant, conformal contact and uniform pattern transfer.

4.2.1. Electrical μ CP (e - μ CP)

Electrical microcontact printing uses a flexible electrode to pattern a thin film of a material that is an electret (i.e., that accepts and maintains an electrostatic potential), probably by injecting and trapping charges.^{26,221,223,224} The electrode was a PDMS stamp coated with ~ 5 nm of chromium (adhesion layer) and ~ 80 nm of gold (electrode material) (Figure 7A). This flexible electrode was brought into contact with a thin dielectric film (the electret, a material such as PMMA) supported on a second electrode (typically n-doped $\langle 100 \rangle$ silicon). Electrical microcontact printing uses the flexibility of the PDMS stamp to allow conformal contact between the top, patterned electrode and the dielectric film. A voltage pulse (10–30 V) was applied between the two electrodes for ~ 10 s with current densities of ~ 10 mA/cm². Charge remained in the electret where the flexible electrode contacted the dielectric film. After

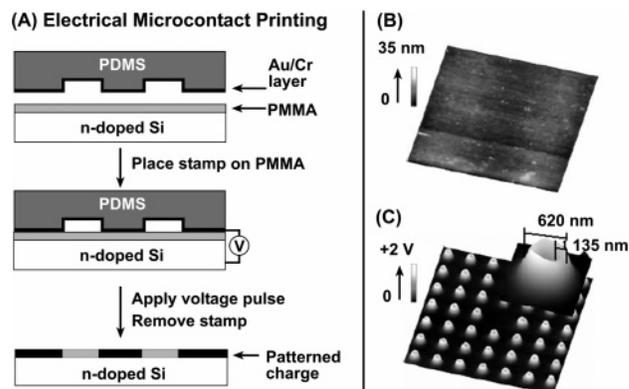


Figure 7. (A) Schematic illustration depicting the procedure for electrical microcontact printing (e - μ CP). Kelvin probe force microscopy (KFM) measurements of a thin film of PMMA after patterning charge by e - μ CP show (B) no change in the surface topography and (C) a surface potential with positive features for a test pattern of 620-nm diameter rings.²²¹ The full-width half-maximum (fwhm) is ~ 135 nm for the positive ring of charge in the plot of surface potential. (Reprinted with permission from ref 221. Copyright 2001 American Association for the Advancement of Science.)

removing this electrode the changes in surface potential across the thin dielectric were measured by Kelvin probe force microscopy (KFM).²²⁵ Figure 7B and C shows the measured topography and corresponding surface potential, respectively, for an 80-nm thick PMMA surface patterned using a current density of 20 mA/cm² (+18 V).²²¹

A number of methods exist for charging electrets, such as corona discharge and tribocharging.^{226–228} Typically, these methods pattern charge in electrets with micrometer-scale lateral resolution. Scanning probe techniques can pattern charge in electrets over multiple length scales and have demonstrated feature densities of 7 Gbits/cm² for 120-nm wide features at a resolution of 100 nm.^{229–231} The writing speed is, however, slow: a pattern covering ~ 1 cm² requires ~ 24 h to write. Alternatively, e - μ CP can pattern charge over areas >1 cm² in less than 20 s with a resolution of ~ 100 nm.²²¹ The minimum feature size, the maximum area that can be patterned in one impression, and the maximum charge density that can be patterned by e - μ CP remains to be established. The stability of the patterned charge (for periods greater than months) is unexpectedly high.²²¹

The patterned, flexible electrode used in e - μ CP is reusable and serves as a tool for fundamental studies of thin film electrets with high lateral resolution. Recent studies have explored the application of e - μ CP in high-density data storage (>5 Gbits/cm²)²²¹ and electrostatic printing of particles, such as graphitized carbon, carbon toner, and iron oxide.^{221,224,232,233} Similar procedures can pattern thin-film waveguides in <90 s over large areas (>1 cm²) by changing the local index of refraction of a doped polymer.²²³

Current limitations of e - μ CP include buckling of the PDMS stamp after metal deposition from thermal expansion and contraction of the surface.²²¹ Depositing metal onto small features or cooling the PDMS prevents buckling.²²³ Other studies have explored alternative designs for the electrodes using a pat-

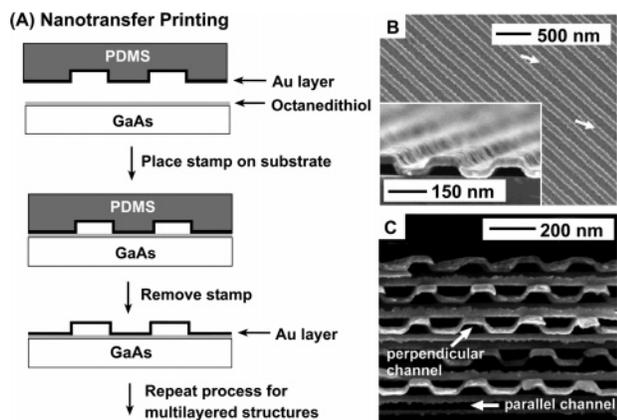


Figure 8. (A) Schematic illustration of one approach to nanotransfer printing (nTP)—transferring thin films from a topographically patterned PDMS stamp by forming bonds between the thin film and the surface chemistry of another substrate. SEM images of (B) a 20-nm gold layer transferred onto a GaAs substrate functionalized with 1,8-octanedithiol and (C) a multilayered stack of 20-nm thick layers of gold containing parallel grooves with each layer oriented perpendicular to the one below.²³⁶ After printing the initial gold layer each subsequent layer is transferred by cold welding.

terned array of nanometer-scale, in-plane edges (further details are provided in section 6.2).⁴¹

4.2.2. Nanotransfer Printing (nTP)

The process of transferring a thin, solid film from a stamp with nanoscale-patterned features to a substrate is referred to as nanotransfer printing.^{39,222,234–242} The stamp can be either a soft or a hard material, such as PDMS or silicon. Figure 8A illustrates a typical procedure for this contact printing technique using a PDMS stamp. The stamp was coated with a continuous layer of gold (~20-nm thick) without an adhesion layer between the gold and the PDMS. This stamp was brought into contact with a substrate coated with a dithiol (e.g., 1,8-octanedithiol).^{222,237} The dithiol formed a SAM on the substrate (GaAs in this case) and the exposed thiol group covalently bound to the gold layer in the regions of contact. Removing the elastomeric stamp from the substrate left the gold layer bound to the SAM and the underlying substrate (Figure 8B). Alternatively, cold welding^{243,244} between two metal surfaces could also transfer the structured metal film. Three-dimensional structures can be fabricated by repeating this procedure (Figure 8C).²³⁶ Another method of releasing the structured film relies on condensation reactions between surface-bound silanols (Si–OH) and/or titanols (Ti–OH).^{222,234} Techniques relying on noncovalent interactions between the metal film and the substrate have also been explored, although the minimum dimensions of transferred features are currently >100 nm.^{239,245}

A number of methods can pattern nanoscale metallic structures such as narrow, periodic lines. Conventional methods include lift-off, wet chemical etching, RIE, and shadow evaporation. These patterning techniques require exposure to high temperatures, basic or acidic solutions, and/or organic solvents. Nanotransfer printing avoids harsh processing condi-

tions and transfers nanostructures in one step.^{222,235} Nanotransfer printing can pattern features with a lateral resolution of at least 70 nm and an edge roughness down to 10 nm.^{222,246}

Nanotransfer printing is well suited for transferring electrodes to fragile surfaces. For example, this contact printing technique can pattern parallel lines and circular dots as electrical contacts on SAMs.²⁴⁶ These discontinuous structures adhere to the substrate under Scotch tape adhesive tests.²³⁵ The components of devices fabricated directly on plastic substrates include complementary inverter circuits, organic thin-film transistors,²³⁵ capacitors,²³⁴ and electrostatic lenses.²²² This patterning technique can also transfer arrays of sacrificial etch masks²³⁶ and stable ferromagnetic stacks of cobalt.²³⁷

The morphology and continuity of the transferred metal structure is important for functional devices. Uniformity of the metal film is dependent on the wetting and grain size of the metal on the stamp. A thin adhesion layer (<2-nm thick) improves the uniformity of a gold layer on the PDMS stamp and in the transferred layer.^{236,237,242} A metal film on an elastomeric stamp can crack from thermal expansion during metal deposition. These cracks can be prevented by rapid deposition of metal (rates ≥ 0.3 nm/s to minimize thermal stress on the PDMS) and by cooling the stamp.^{223,237} The stress in the metal film from thermal expansion is also avoided by depositing the metal onto a stamp with a higher thermal conductivity than PDMS (e.g., silicon or gallium arsenide). The surface of these stamps must, however, be modified with a release layer. Mechanical stress during printing can also introduce cracks into the metal structure as indicated by the arrows in Figure 8B.²³⁶ The consequences of mechanical deformations on nanofabrication using nTP remain to be established.

An alternative approach to printing structured materials is decal transfer printing.^{150,151} This process transfers a structure (e.g., PDMS membrane or isolated PDMS features) from one planar surface to another. The PDMS decals can be made to adhere reversibly to the first substrate (i.e., a PDMS slab)^{247,248} while forming covalent bonds with the second substrate. The PDMS slab serves as a handle for patterning continuous or discontinuous features that are otherwise difficult to manipulate. Decal transfer printing can transfer submicrometer features, but extending this technique to nanoscale features will require further investigation of the interfacial adhesion between the PDMS (or other) substrate and the decal.

4.3. Fundamental Limits of Printing

A number of factors determine the smallest features that can be printed by soft lithography. The fundamental limits to printing are determined by three main constraints: (i) minimum size of features in the stamp; (ii) lateral dimensions and resolution of the transferred material; and (iii) preferential adhesion of the printed material to the second surface.

The smallest feature in the stamp depends on the size of features within the master, the fidelity of the

molding process, and the ability of the mold to retain nanoscale features. Further details on these limitations are provided in section 3.3. Distortion of the stamp while in contact with the printed surface also limits the minimum size of the transferred feature. The elastic deformation of a PDMS stamp can collapse high-aspect ratio features.^{156,158} A composite stamp of h-PDMS with a 184-PDMS backing can, however, retain nanoscale features without collapse.^{159,161}

Resolution and lateral dimensions of nanoscale features transferred from these stamps depends, among other things, on the interaction of the “ink” with the printed surface. Lateral spreading of the pattern (either by transport across the surface or by transport through the vapor) can result from diffusion of molecular ink on the printed surface. For example, alkanethiols patterned on gold by μ CP while immersing the substrate in water illustrates the effect of lateral surface diffusion of alkanethiol across gold on broadening of the printed pattern.¹⁹⁴ In this example the lateral dimensions of the exposed gold decreased from 500 to \sim 35 nm by a time-dependent surface-mediated diffusion as the stamp was left in contact with the substrate for longer intervals of time. Diffusion of the ink and blurring of the features can be minimized by using high-molecular-weight inks. Patterns of dendrimers printed by μ CP can have lateral dimensions as small as \sim 40 nm.^{220,249} The outline of the edge of features printed using μ CP often follows the edges of grains in the metal film used as a substrate; grain size is, thus, also an important determinant of resolution.

The resolution and minimum feature size of thin films transferred by nTP also depends on the integrity of the transferred material. The wetting of the stamp by a metal determines the minimum thickness and lateral dimension of isolated features. The grain size of the metal determines the thickness required to produce a continuous film. For example, the minimum thickness of a continuous film of Pd (grain size of \sim 20 nm) is less than that required for Au (grain size of \sim 50 nm) when deposited by electron-beam evaporation onto titanium-coated silicon.¹⁹⁰ Cracks in the metal film can also form as a result of mechanical deformation during printing.²³⁶ Other sources of defects in the printed pattern include disparities in the stamp or on the printed surface.

The preferential adhesion of the material to be printed onto a second surface is a third limiting factor in printing nanoscale features. The stamp can be coated with a release layer to assist the removal of this material (e.g., a metal film) from the stamp by decreasing the surface free energy of the stamp. This release layer can, however, also decrease the wetting of metal deposited onto the stamp.²³⁷ The formation of covalent bonds between the transferred material (e.g., molecular ink or metal film) and the printed surface also improves release from the stamp.^{222,234,236,237}

4.4. Summary

Printing materials onto a surface using a topographically patterned stamp changes the properties

of the surface in well-defined, nanometer-scale regions. High-resolution printing normally requires a mechanically compliant stamp. The surface can be modified with covalently bound molecules (e.g., SAMs) or thin films (e.g., gold or silica). The printed material can have applications as etch resists, reversible or permanent electrical contacts, patterns for use in biotechnology, and bits of information in high-density data storage. Resolution is, however, limited by surface diffusion of printed molecules and distortion of the features within the stamp during printing. Different materials are being developed to improve the fidelity of pattern transfer from nanometer-scale features. These printing techniques are easily implemented and attractive because the stamps are generated inexpensively from readily available commercial precursors.

5. Scanning Probe Lithography (SPL) for Nanofabrication

Scanning probe lithography provides a versatile set of tools for both manipulating and imaging the topography of a surface with atomic-scale resolution.^{31–34} At present, these tools seem well suited for applications in research but will require substantial development before they can be used for patterning large areas in manufacturing. The most important SPL techniques include scanning tunneling microscopy (STM), atomic force microscopy (AFM), and near-field scanning optical microscopy (NSOM). A striking example of the potential of these techniques for nanoscale fabrication is the precise positioning of individual Fe atoms with an STM tip (Figure 9A,B).^{28,52} This atomic-scale manipulation is interesting scientifically but is not yet a practical technology. Our review of the current capabilities of SPL for nanofabrication is brief as this topic is extensively reviewed elsewhere.^{32,34,53}

5.1. Serial Patterning of Surfaces Using SPL

Scanning probe lithography is a versatile method for depositing clusters of atoms or molecules onto a surface in a well-defined pattern.³⁰ One approach to deposit nanoparticles or molecules selectively onto a surface is dip-pen nanolithography (DPN).^{30,31,250–252} An AFM tip is “inked” with a solution of the material to be transferred to the surface. The material adsorbed onto the AFM tip transfers to the surface in an arbitrary pattern “written” with the scanning probe (Figure 9C). This technique can, with care, reproducibly pattern lateral features as small as 50 nm.³² Molecules patterned by this technique include SAMs for binding oligonucleotides, proteins, and viruses.^{253–256} Similar SAMs can also mask the substrate during wet etching to pattern nanostructures of metals such as Au, Pd, and Ag (Figure 9D).²⁵⁷ This process can fabricate trenches with lateral dimensions from 12 to 100 nm.²⁵⁸ The mechanism of material transfer by DPN is not yet clear. One possibility is that water between the tip and surface mediates the process;²⁵⁹ another is the transfer of solid material as a result of tip–surface interactions. The spreading of the ink on the substrate depends

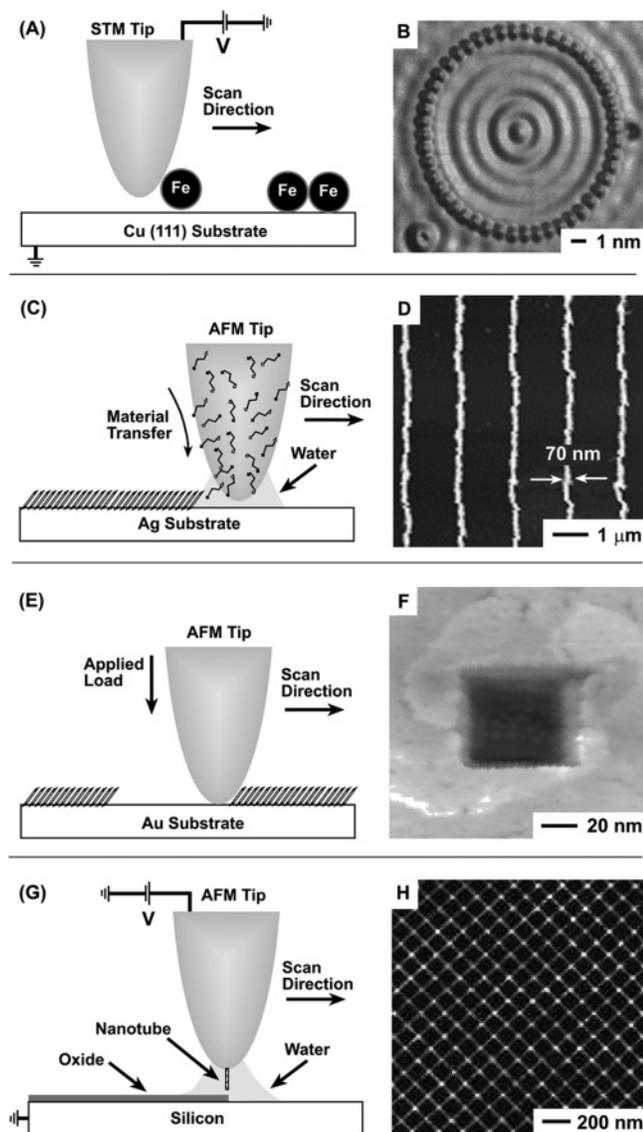


Figure 9. Schematic representations of four approaches to scanning probe lithography, and patterns produced using them: (A) Scanning tunneling microscopy (STM) can position atoms on a surface with high precision to generate patterns, such as (B) a quantum corral of a 48-atom Fe ring formed on Cu enclosing a defect-free region.⁵² (Reprinted with permission from ref 52. Copyright 1993 American Association for the Advancement of Science.) (C) Dip-pen nanolithography can direct the deposition of SAMs (e.g., 16-mercaptohexadecanoic acid) on Ag as an etch resist to pattern (D) 70-nm wide features.²⁵⁷ (E) Nanoshaving can remove regions of SAMs to pattern features such as (F) a square hole within octadecanethiolate SAMs on Au.²⁶⁶ (G) Scanning electrochemical oxidation with a carbon nanotube-modified AFM tip can selectively oxidize a surface to pattern (H) 10-nm wide (2-nm tall) silicon oxide lines spaced by 100 nm.²⁷⁸ (Reprinted with permission from ref 278. Copyright 1999 American Institute of Physics.)

on the humidity, the reactivity of the ink with the substrate, the radius of curvature of the probe, and the linear velocity of the probe.^{259–264}

Another approach to nanofabrication by SPL is the selective removal of material from a surface by force-induced patterning.^{265–267} An AFM tip in contact with the surface can displace SAMs in a process referred to as nanoshaving (Figure 9E).²⁶⁶ This process removes SAMs from a surface in a well-defined pattern

(Figure 9F). Irregularities in these patterns can result from variations in surface topography and, therefore, a nonuniform contact between the tip and substrate. A second material (e.g., SAMs or nanoparticles) can replace the removed film.^{265,268} This substitution lithography is a convenient method for patterning multiple types of SAMs on a surface. Mechanical displacement of a thin polymer film with an AFM tip, or “nanoplowing”,²⁶⁹ can also generate nanoscale holes or trenches. These nanoscale pits are templates for the growth of nanowires²⁷⁰ or the formation of nanosized electrical contacts with the supporting substrate.²⁷¹

Scanning probe lithography is also used to modify a surface chemically.²⁷² One example is the localized oxidation of a surface (metal, semiconductor, or SAM) in a pattern scanned by a conductive AFM or STM tip (Figure 9G).^{267,272–277} A local electric field between the conductive tip and the surface induces oxidation of the surface. Typically, this method can generate ~50-nm wide features. A carbon nanotube-modified AFM probe can, however, pattern ~10-nm wide lines of silicon oxide on a silicon hydride surface (Figure 9H).²⁷⁸ A conductive AFM tip can also locally modify organosilane SAMs to direct the deposition of Au₅₅ nanoclusters.^{279,280} Another SPL method for chemically modifying a surface is photochemical oxidation by NSOM.^{281–283} For example, the photochemical oxidation of SAMs of mercaptoundecanoic acid (with 244-nm light), followed by selective wet chemical etching of the unmasked substrate, can pattern ~55-nm wide trenches in gold.²⁸¹

The commercial availability of AFM, STM, and NSOM make these tools convenient for nanofabrication. These instruments are also capable of nanoscale registration. This approach to writing nanoscale patterns with a single tip is, however, fundamentally slow. The serial nature of SPL results in a low sample throughput. Single-probe methods are probably restricted to research applications and possibly to fabrication of customized patterns or mask repair.

5.2. Parallel Patterning of Surfaces Using SPL

A practical approach to SPL for large-volume, parallel production may emerge by simultaneously writing patterns with multiple probes.^{53,250,251,284–288} An array of cantilevers scanned in parallel may allow higher sample throughput. The concept of the “Millipede” was developed as a 2D array of independently addressable AFM probes for high-density data storage.^{53,284–286,289,290} Each probe in this array can be mechanically deflected in the vertical direction and resistively heated. These arrays of cantilevers can locally heat a thin polymer film to pattern ~40-nm wide holes with feature densities >10¹¹/in.^{2,284,285} A parallel approach to patterning surfaces by DPN is also being explored.^{250,251,287,288} The current capabilities of parallel DPN include writing multiple copies of a pattern using a linear array of passive (i.e., nonactuated) probes or writing a series of different patterns (e.g., alkanethiols in the pattern of numerals 0–9) using thermally actuated probes.²⁸⁷

5.3. Summary

Scanning probe lithography can precisely position atoms on a surface and selectively deposit or remove regions of etch resist to pattern surfaces. These techniques may find applications in mask or device repair and information storage. Parallel approaches in SPL are being developed to overcome the serial limitations of standard SPL technologies. Surface diffusion of molecular inks and colloidal suspensions broadens the features patterned by SPL. It is also challenging to generate reproducible structures between scans because of variations in the surface topography of the substrate and differences in the shape of the tip (and variations in this shape with time and use).

6. Edge Lithography for Nanofabrication

We define edge lithography as either pattern transfer directed by the edge of a feature^{36,42,291,292} or the process of transforming a feature that is thin in the vertical direction into a feature that is thin in the lateral direction.^{37,39,40,293} General interest, as measured by volume of publications, in these two areas has increased markedly in the past few years.^{35–43} These methods are, currently, limited in the types of patterns they can form but can be used to pattern arrays of <100-nm structures in parallel for a range of materials.

6.1. Pattern Generation Directed by Topography

In this section we discuss varieties of edge lithography that use the edge (e.g., the perimeter or the outline) of a topographic feature to generate nanoscale structures. We divide this area into four strategies for generating nanostructures: (i) depositing material at step edges of crystalline lattices;^{43,294–297} (ii) adding or removing material at edge-defined defects in SAMs;³⁶ (iii) depositing or undercutting at lithographically defined step edges;^{38,298} and (iv) patterning photoresists at regions defined by vertical edges in a soft stamp using phase-shifting photolithography.^{42,292}

6.1.1. Material Deposition at Step Edges of Crystalline Lattices

The exposed edges of steps within a crystalline lattice can have properties that are different from those of the bulk material. For example, gas-phase catalytic studies have shown that the exposed edges of the catalyst can promote a number of reactions and are often the sites of highest reactivity.^{295,299–301} The step edges on single-crystalline surfaces can also direct the growth of metal nanostructures.^{43,294,296,297,302–306} Particles of Cu, Co, and Ag have been deposited at the step edges of crystalline metallic substrates (e.g., Mo (110), Ag (111), and Cu (111)).^{303–305,307} These metals are often deposited by physical vapor deposition.

Material deposition at atomic step edges has been extended to the growth of continuous nanowires by electrodeposition on highly oriented pyrolytic graphite (HOPG) (Figure 10A–C).^{43,308} This approach can

generate nanowires of metals (e.g., Ag, Pd, Cu, and Au), oxides (e.g., MoO_x and Cu₂O), and semiconductors (e.g., MoS₂ and Bi₂Te₃).^{43,308–313} These nanowires can have lateral dimensions down to ~15 nm⁴³ and can be used in the fabrication of gas sensors.^{310,314} For example, palladium nanowires, transferred to a cyanoacrylate polymer film, can be used to detect the presence of hydrogen gas.^{310,315,316} Nanowires grown by this method are, however, randomly positioned on the substrate in a pattern determined by the orientation and spacing of the step edges in the HOPG substrate. Their grain structures and edge roughness have not been characterized. The diameter of these nanowires can also vary across the substrate, and nanoparticles can nucleate on defects in the substrate.

6.1.2. Patterning at Edge-Defined Defects in SAMs

Another strategy for patterning nanostructures by edge lithography is the selective deposition or removal of material in regions defined by defects at the edges of topographic features.^{36,291,317,318} For example, SAMs form polycrystalline lattices on a planar metallic surface but remain disordered at the edges of this surface.^{35,36} Sharp metal corners within a topographically patterned metal substrate prevent the formation of well-ordered SAMs and expose the underlying metal at these edges.^{36,291,317} Selectively etching the exposed metal transfers the outline of the patterned metallic topography into the underlying film with line widths as small as ~50 nm.

A modification of this technique included a thin titanium layer (~5-nm thick) between a patterned silver film and a planar supporting layer of silver.³¹⁸ Immersing this patterned substrate in a solution of alkanethiols formed SAMs on the silver but not on the titanium. The exposed edge of titanium formed a gap (~5-nm wide) in the SAM-coated substrate. This edge served as a well-defined nanoelectrode for the electrodeposition of copper.³¹⁸ Electrodeposition on edges made by engineering defects in SAMs has generated nanowires with lateral dimensions as small as 70 nm.

These edge lithographic techniques generate nanostructures in a pattern defined by the outline of a topographic template. Photolithography can pattern regular arrays of topographic features, and electrodeposition on edges within this well-defined surface can generate aligned nanowires.³¹⁸ Removing an array of parallel nanowires with Scotch tape transfers these nanostructures to a transparent, flexible substrate and generates an efficient optical polarizer.³¹⁸ Nanowires supported on this adhesive substrate can also conform to a curved or flat substrate.

6.1.3. Controlled Deposition and Undercutting at Lithographically Defined Step Edges

Thin-film deposition onto a topographic template and selective etching of the substrate can also generate nanoscale features. For example, shadow evaporation of metal onto the side walls of topographic features, followed by the selective etching of the substrate, generates narrow, vertical structures.³¹⁹ White et al. used this method to fabricate an array

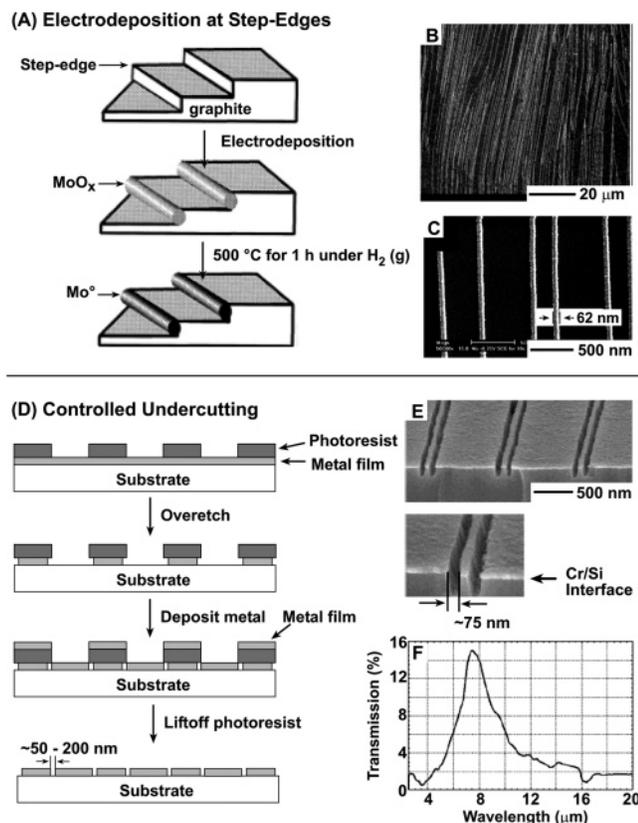


Figure 10. (A) Schematic illustration of the process used to generate nanowires at step edges of a graphite surface by electrodeposition of a material such as molybdenum oxide (MoO_x).⁴³ SEM images (B,C) show dense arrays of MoO_x nanowires deposited from 1.0 mM MoO_4^{2-} . (Reprinted with permission from ref 43. Copyright 2000 American Association for the Advancement of Science.) (D) Schematic illustration of the use of controlled undercutting to pattern trenches with lateral dimensions as small as ~ 50 nm. (E) SEM images for a cross-section of ~ 75 -nm wide trenches transferred into a silicon substrate through a patterned chrome mask. (F) An infrared transmission plot showing the frequency selectivity of 100-nm wide trenches in Al on a CaF_2 substrate, prepared by selective undercutting.²⁹⁸ (Reprinted with permission from ref 298. Copyright 2001 Wiley-VCH Verlag GmbH.)

of 30-nm wide lines of silica with heights of ~ 300 nm.³⁸ Depositing a low-temperature oxide uniformly over topographic features and etching the substrate by RIE can also pattern nanoscale features at the outline of each feature.³²⁰ Somorjai et al. used this method to generate ~ 10 -nm wide vertical structures.³²¹

Patterned arrays of nanostructured trenches can be fabricated by the controlled undercutting of topographic features using isotropic wet etching, followed by deposition of a thin film (Figure 10D).²⁹⁸ In this approach to nanofabrication the initial step was to pattern a photoresist supported on a metal-coated substrate (e.g., chromium on silicon). The exposed metal film was isotropically wet etched with controlled undercutting of the photoresist. Coating this substrate with a second metal film, followed by lift-off of the photoresist, produced nanostructured trenches at the edges of the photoresist (Figure 10E). This method has patterned well-defined trenches with lateral dimensions as small as ~ 50 nm.²⁹⁸

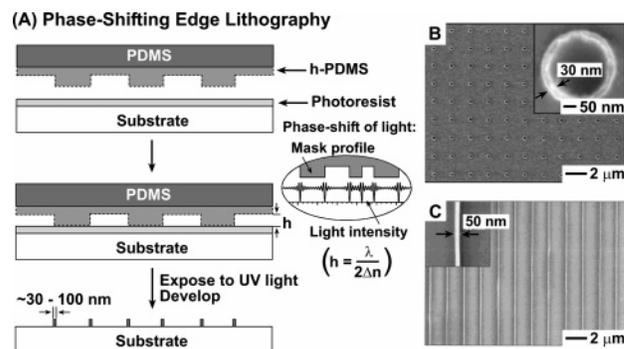


Figure 11. (A) Schematic illustration of phase-shifting edge lithography using a topographically patterned composite PDMS stamp—“hard” PDMS (h-PDMS)¹⁶¹ with a soft PDMS backing¹⁵⁹—in contact with a photoresist. The schematic depicts the intensity profile for a destructive modulation of the phase-shifted light—a constructive modulation is also possible (see text for further details). The phase shift is optimal when the thickness of the air gap (h) is equivalent to the ratio of the wavelength (λ) of light to twice the change in refractive index (Δn) between PDMS and air. The SEM images show (B) 30-nm wide rings¹⁶² and (C) 50-nm wide lines¹⁵⁹ patterned in a positive-tone photoresist using phase-shifting edge lithography with a PDMS stamp and destructive modulation of the phase-shifted light.

Controlled undercutting of photoresist has also patterned ~ 50 -nm wide islands of gold for growing ZnO nanowires.³²² The controlled undercutting of thin films is not limited to semiconductor substrates. For example, patterning an aluminum film supported on a calcium fluoride (CaF_2) substrate generated a frequency-selective surface (Figure 10F).²⁹⁸

Each of these techniques is limited in its ability to pattern arbitrary features by the features whose edges are being used and by the characteristics of light. Photolithography and electron-beam lithography can pattern regular arrays of topographic features, and nanoscale structures generated at the step edge of these features are regularly spaced. Patterning intersecting (crossing) lines of metal using these methods is, however, not straightforward.

6.1.4. Phase-Shifting Edge Lithography

Another approach to nanofabrication directed by the edges of a topographic feature is near-field phase-shifting photolithography (Figure 11A).^{42,162,241,292,323–325} In this technique the vertical edges of a transparent, topographically patterned substrate induce abrupt changes in the phase of incident, collimated light over short distances (an edge in a conformal, transparent, PDMS stamp). This shift in phase of the incident light creates narrow regions of constructive and destructive interference. Phase-shifting photolithography uses this interference to project “dark” or “bright” regions of incident light onto the surface of a photoresist. The smallest lateral dimensions are produced when the light has a phase shift of π radians at the photoresist–mask interface (Figure 11A).

Masks for phase-shifting edge lithography must be transparent and situated as close as possible to the films of photoresist. (Ideally the mask and photoresist should be in conformal contact.) These

requirements limit the choice of materials. A hard mask for phase-shifting edge lithography (e.g., quartz) is expensive to design and requires accurate control of the distance between the phase mask and the photoresist; accidental contact between the two easily damages or contaminates the mask. An elastomeric mask is much less expensive to fabricate and use. A PDMS mask spontaneously and nondestructively achieves conformal contact with the photoresist; conformal contact eliminates any gap between the mask and the resist and places the resist directly in the optical near-field of the mask. Phase-shifting edge lithography using an elastomeric phase mask has patterned features as small as ~ 30 nm (Figure 11B,C).^{162,292,323,324,326,327} Soft masks for phase shifting can also conform reversibly to nonplanar surfaces.^{162,292,323,328}

Near-field phase-shifting photolithography has been used to fabricate a number of simple patterns, such as rings and lines of photoresist (Figure 11B,C). These patterns can be transferred into a metal film by lift-off or selective wet etching.^{162,292,323,327} The narrow photoresist features can also mask a substrate during RIE. This method produced uniform, single-crystalline silicon nanostructures with well-defined features as small as 40 nm and lengths >1 cm.³²⁶ Components of devices fabricated by phase-shifting edge lithography include optical polarizers³²⁹ and gates for organic transistors³²⁷ having dimensions as small as ~ 100 nm. An array of patterned features in Al can generate a frequency-selective optical filter,³²⁸ and nanostructured holes patterned in photoresist can direct the crystallization of various salts¹⁶² or the deposition of nanoparticles.³³⁰

Phase-shifting lithography using a soft, conformal mask requires that the mask have vertical, straight sidewalls. Distortions in the mask broaden the features in the photoresist. A composite stamp (Figure 11A) of h-PDMS with a "soft" PDMS backing such as Sylgard 184 improves the fidelity of the sidewalls in comparison to a stamp of only Sylgard 184-based PDMS. These composite stamps can pattern ~ 30 -nm features by phase-shifting edge lithography.¹⁶²

6.2. Generating Nanostructures by Exposing the Edge of a Thin Film

A second type of edge lithography takes advantage of the numerous methods^{331,332} that can grow thin films over large areas with a thickness between 1 and 50 nm. Converting these films— which are thin in the vertical direction—into structures that are thin in the lateral direction is an approach to fabricating nanostructures.^{37,39,293,333,334} There are now three demonstrated approaches to exposing a nanostructured edge: (i) fracturing a thin film;³⁷ (ii) sectioning an encapsulated thin film;^{40,335} and (iii) reorienting posts capped with a thin film.⁴¹

6.2.1. Edges by Fracturing Thin Films

Thin films deposited onto a crystalline semiconductor substrate, such as silicon, can be exposed as a uniform narrow edge by fracturing the substrate. Multilayered structures grown by molecular beam epitaxy (MBE) are the most common substrates for

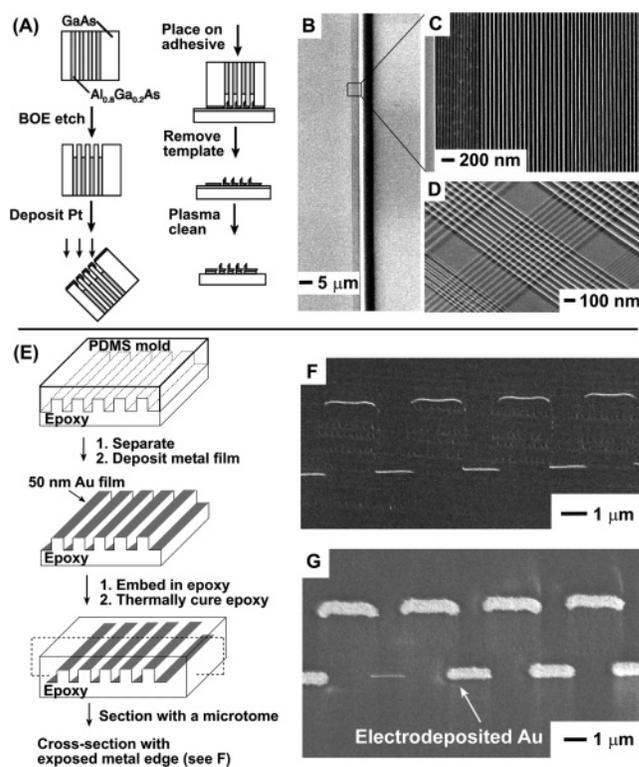


Figure 12. (A) Schematic illustration of a method to create nanowires by physical vapor deposition on a structured edge, which was fabricated by molecular beam epitaxy (MBE) and etching in buffered oxide etch (BOE).³⁹ (Reprinted with permission from ref 39. Copyright 2003 American Association for the Advancement of Science.) (B,C) SEM images showing the edge of the MBE-grown substrate with an array of deposited Pt nanowires. The higher resolution micrograph (C) shows 10-nm diameter Pt nanowires with a pitch of 60 and 30 nm. (D) SEM image of a cross-bar array of Pt nanowires fabricated from two sets of nanowires transferred to adhesive substrates. (E) Schematic diagram showing the fabrication of a patterned array of epoxy-embedded conducting metal edges.⁴⁰ (F) SEM image of 50-nm thick Au lines exposed by sectioning with the glass knife of a microtome. (G) Electrochemical deposition of a metal onto the exposed metal edge identified the conductive regions.

this approach to nanofabrication. For example, Pfeiffer et al. fractured multilayered MBE-grown substrates to direct the growth of quantum wires and quantum dots.^{37,336,337} An MBE-grown substrate consisting of alternating layers of AlGaAs and GaAs was also used to fabricate an array of field-effect transistors (FETs) with ~ 20 -nm gate lengths.³³⁶

The edge of a multilayered substrate that has been fractured can also template the formation of parallel nanowires by physical vapor deposition (Figure 12A).³⁹ Selectively etching one of the components (e.g., AlGaAs in a buffered oxide etch) reveals an array of narrowly spaced grooves. These grooves are partially coated with a metal by selective angle deposition to generate parallel nanowires supported on the fractured edge (Figure 12B,C). The width of the groove, corresponding to the original thickness of the etched film, determines the spacing between the nanowires. Modifying the thickness of each layer of the substrate will change the spacing between the nanowires and the width of the nanowires.

An adhesive tape can remove the deposited lines of metal from the edge of the substrate. Free-standing Pt nanowires have been fabricated with diameters down to 8 nm at a pitch of ~ 16 nm.³⁹ Parallel nanowires supported on adhesive substrates can be overlapped perpendicular to each other to create a cross-bar array (Figure 12D). A narrow edge can also direct the assembly of nanorods.³³⁸ For example, nanorods of cadmium selenide (7-nm diameter, 35-nm long) can align along an exposed edge of ZnS (5-nm wide) functionalized with SAMs of hexanedithiol.

6.2.2. Edges by Sectioning Encapsulated Thin Films

A nanostructured edge can also be fabricated by embedding a thin film in a soft matrix and cutting or sectioning the matrix to expose its cross-section.^{40,335} An early approach to exposing the edge of an embedded thin film was to polish the substrate with an abrasive film (e.g., silica-coated paper).³³⁵ In this approach a Pt film deposited on mica was encapsulated in an epoxy matrix. Sanding this soft matrix exposed the edge of the Pt. Another approach to expose the edge of the encapsulated film is to section a polymer-encased metal film with the glass knife of a microtome.⁴⁰ This approach to edge lithography can be combined with soft lithography to generate a patterned edge structure (Figure 12E). The surface roughness of the sectioned epoxy was ~ 10 nm. This smooth surface encapsulated arrays of isolated, narrow gold lines with line widths as small as 50 nm (Figure 12F).

Exposed edges can direct the deposition of material into a well-defined pattern. For example, these edges can be electrically addressed from the opposite side of the encapsulating matrix for the electrodeposition of metal (Figure 12G).⁴⁰ This backside electrical connectivity cannot be easily achieved for structures patterned by conventional techniques. We believe the combination of edge lithography with molding and embossing will be a useful method for generating substantially more complex features than those currently demonstrated.

A major challenge in fabricating encapsulated nanostructures by sectioning a soft matrix is minimizing delamination at the matrix–metal interface. Sectioning the matrix at temperatures below -120 °C significantly minimized the delamination between epoxy and an embedded metal film.⁴⁰ Exposing the surfaces to an oxygen plasma before embedding also improved adhesion between the interfaces.

6.2.3. Edges by Reorientation of Metal Capped Posts

Ordered arrays of nanoscale edges have been patterned by capping an array of posts with a thin film and tipping each post onto one side (Figure 13A).⁴¹ Arrays of epoxy posts were patterned by photolithography on a silicon substrate. A thin metal film, deposited by selective angle deposition, coated the top and part of one side of each post. These structures (“microdominos”) fractured from the supporting substrate under a horizontally applied shear force. They collapsed in a uniform pattern determined by the direction of the applied shear (Figure 13B,C). These patterned, collapsed arrays can cover large

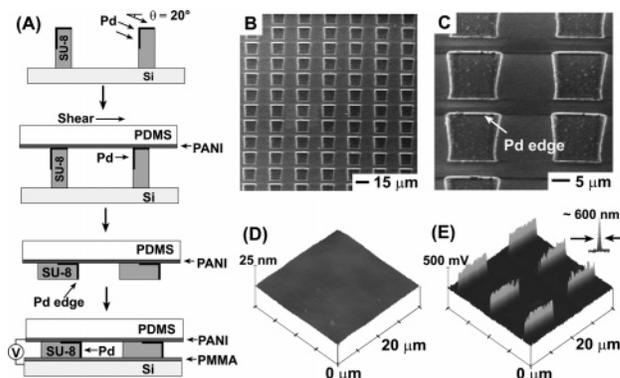


Figure 13. (A) Schematic illustration of an array of microdominos (posts of an epoxy-based photoresist) uniformly collapsed by applying a horizontal shear using a slab of PDMS. Selectively depositing a Pd film to coat the top and part of one side of each post prior to collapse generated an array of nanometer-scale Pd edges after collapse.⁴¹ (B) SEM images of the array of collapsed microdominos adhering to the PDMS slab, and (C) a ~ 15 -nm wide edge of Pd on a collapsed post. (D) Topography and (E) surface potential of a 100-nm thick PMMA surface after patterning regions of charge (~ 400 mV peak surface potential). The inset in E shows the side profile for one region of charge. (Reprinted with permission from ref 41. Copyright 2004 Wiley-VCH Verlag GmbH.)

areas (> 1 cm²). The reorientation of the microdominos also generates an array of metal features (edges) with nanometer dimensions in the plane of the array.

The collapsed microdominos adhere to the PDMS slab that is used to apply the shear force. If a conducting polymer [e.g., polyaniline (PANI)] had been grafted onto the surface of the PDMS before using it to shear the microdominos, this layer established an electrical connection to each of the arrayed edges.⁴¹ This array of narrow electrodes supported on a conducting, elastomeric substrate can be used as a flexible electrical contact for printing charge into electrets (Figure 13D,E). Multiple regions of charge can be patterned in parallel by simultaneously addressing all of the nanoscale edges.

A nonuniformly applied shear force can result from uneven contact between the microdominos and the horizontally translated substrate. An elastomeric slab, such as PDMS, applies a shear force evenly across the substrate by conforming to asperities in the surface. A limitation of the current approach is the inability to address each nanoscale edge individually. Patterning the conductive substrate in contact with these edges may overcome this limitation.

6.3. Summary

Edge lithographic techniques are, currently, restricted to generating certain, limited types of line structures (e.g., noncrossing lines) in one step of fabrication. Crossed lines can sometimes be generated by stacking features.^{39,40} These techniques are still being developed as tools for research. Recent developments include “wiring-up” nanostructures to external magnetic or electric fields^{40,41} and directing the formation of parallel nanowires with applications in nanoelectronics³⁹ and tunable optical polarizers.³¹⁸ We believe the formation of more complex nanostruc-

tures will result from a combination of edge lithography with other—conventional and unconventional—nanofabrication techniques.

7. Self-Assembly for Nanofabrication

Self-assembly is defined as the spontaneous organization of two (or more) components into larger aggregates using covalent and/or noncovalent bonds.³³⁹ We described a number of techniques for nanofabrication that use top-down approaches to patterning nanostructures. Self-assembly, a bottom-up approach to nanostructures or nanostructured materials, is a second strategy for nanofabrication. This approach relies on cooperative interactions of small components that assemble spontaneously in a predefined way to produce a larger structure in two or three dimensions.

We will focus on two types of self-assembly: (i) nontemplated self-assembly, where individual components interact to produce a larger structure *without* the assistance of external forces or spatial constraints, and (ii) templated self-assembly, where individual components interact with each other *and* an external force or spatial constraint. In this section we review recent advances in both nontemplated and templated self-assembly and evaluate the current status of self-assembly for nanofabrication. For other aspects of self-assembly we direct the interested reader to recent reviews.^{46,340–344}

7.1. Nontemplated Self-Assembly

One of the most appealing aspects of self-assembly is the *spontaneous* assembly of components into a desired structure. The notion that a fabrication strategy requires only the mixing of components to achieve an ordered structure is appealing both for its simplicity and its potential efficiency. We refer to this type of self-assembly as “nontemplated self-assembly.” Examples of materials fabricated using this approach include SAMs⁵¹ and structures that self-assemble from block copolymers^{345,346} and nanoparticles.^{343,347} Recent advances in the fabrication of functional nanostructures using self-assembly include self-assembled arrays of magnetic nanoparticles that can be used as magnetic data storage devices³⁴⁸ and self-assembled arrays of nanorods that display birefringence.³⁴⁹

Nontemplated self-assembly, while attractive for its minimalist use of materials and energy (in contrast to conventional lithography), is not widely used for nanofabrication. Examples where this approach is used for nanofabrication include patterning arrays of nanoelectrodes³⁵⁰ and generating arrays of metal nanoparticles for sensing biomolecules.³⁵¹ Self-assembly is, however, prone to producing defects, and the perfect periodicity of self-assembled structures from nanoscale components is generally limited to micrometer-sized areas. The components also have a limited number of different ordered arrangements and generate a limited number of functional structures.

7.2. Templated Self-Assembly

By templating self-assembly it is possible to introduce an element of pattern into the self-assembled

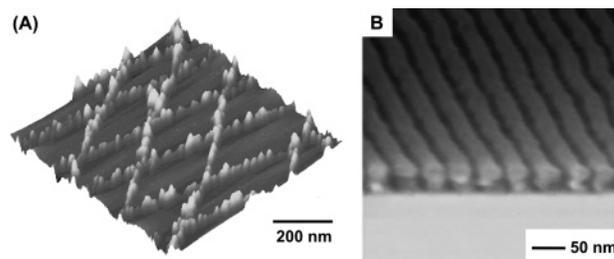


Figure 14. (A) AFM image of a 2D array of DNA strands that templates the electroless deposition of palladium nanowires.³⁶⁴ (B) SEM image of poly(styrene-*block*-methyl methacrylate) self-assembled onto a photopatterned template of SAMs with a periodicity of 48 nm.³⁶⁶ (Reprinted with permission from ref 366. Copyright 2003 Macmillan Magazines Ltd.)

structure and sometimes increase the order of the self-assembled structure. Self-assembly can be directed using surface topography, electric and magnetic fields, or shear forces. Templated self-assembly³⁵² often uses top-down strategies to fabricate components that direct the bottom-up assembly of molecules, macromolecules, or colloidal particles. Templated self-assembly is an alternative to nontemplated self-assembly for the controlled fabrication of patterned structures with nanometer-scale local order and for the generation of micrometer-size, or larger, domains of defect-free patterns.

7.2.1. Templating from Molecules

Molecular and supramolecular chemistry^{45,339,353,354} can produce structures that range in size from 1 to 100 nm and beyond.^{46,174,353,355–359} Generation of these nanostructures has, largely, remained an academic exercise in design and synthesis because they are not functional—they are not, for example, electrical conductors, transistors, or motors. These organic nanostructures have, however, been used as templates to mask the deposition of metal³⁶⁰ or guide the growth of metal nanoparticles^{361–363} and nanowires (Figure 14A).³⁶⁴ The inorganic nanoparticles or nanowires are often fabricated at electrodes and are electrically conductive.³⁶⁵

The combined use of a top-down approach, such as photolithography, and bottom-up self-assembly has been used to pattern block copolymers (Figure 14B).^{366–369} Patterns of oxidized SAMs (or random copolymers)³⁷⁰ on silica can be produced using extreme ultraviolet interferometric lithography that exhibit a periodicity on the order of the lamellar spacing of poly(styrene-*block*-methyl methacrylate) copolymer (that is, in this instance, ~48 nm). When this block copolymer is allowed to self-assemble on the patterned SAM (or random copolymer), this pattern acts as a template that guides the phase separation of the polymer. Annealing a block copolymer film confined by physical boundaries can also direct the assembly of the copolymer into a regular structure.^{371,372} These methods illustrate the use of templating to overcome the major disadvantages of nontemplated phase separation as a method of generating regular structures: that is, the high level of defects, the inability to control or pattern the phase-

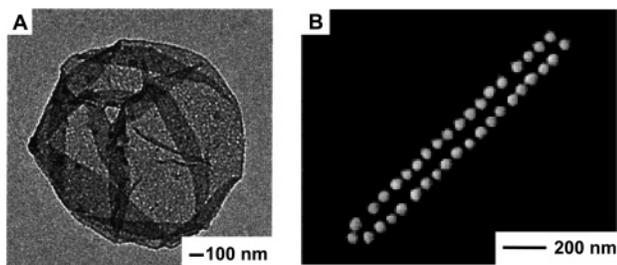


Figure 15. (A) Transmission electron microscopy (TEM) image of a polymer shell fabricated using layer-by-layer assembly of alternating layers of poly(styrenesulfonate) and poly(allylamine hydrochloride) assembled onto a spherical template.³⁸⁰ In this example the template—a melamine—formaldehyde bead—has been selectively removed. (B) SEM image of 50-nm diameter gold nanoparticles assembled into wells defined by electron-beam lithography.⁴⁰⁰ Capillary forces generated by an evaporating solvent directed the particles into these wells.

segregated regions, and the uncontrolled drift in the pattern over nonlocal dimensions.

7.2.2. Templating from Particles

Charged polymers, or polyelectrolytes, can be used to modify the surfaces of colloidal particles.^{373–375} The electrostatic attraction between a charged surface and a charged macromolecule is sufficient for adsorption. Often the charged polymer has excess charge and reverses the charge on the surface.³⁷⁶ This reversal of charge permits a second polymer, with charge opposite to the first, to assemble on the first layer of polymer. The layer-by-layer assembly^{377,378} of polyelectrolytes can be repeated to produce robust multilayers of self-assembled polymers.

Layer-by-layer assembly has also been used to fabricate hollow colloidal particles (Figure 15A).^{379–383} A multilayer polyelectrolyte shell around a colloidal particle is robust enough to survive the removal of the core, generally achieved by dissolving the core template in a solvent. The hollow and permeable shell that remains encapsulates an attoliter volume.^{377,384} Colloidal particles have also been used to template the self-assembly of nanoparticles.^{385–388} Typically, the diameter of these particles is larger than 100 nm, but the assembly of thin, metallic shells may be important for nanofabrication.

7.2.3. Templating Using External Forces

Nanospheres that are monodisperse in size and shape self-assemble to produce thin films of close-packed, ordered lattices. Electric³⁸⁹ and magnetic fields³⁹⁰ as well as shear forces³⁹¹ and spatial constraints^{392,393} have been used to direct the assembly of nanoparticles and nanorods into different configurations. For example, template-assisted self-assembly³⁹⁴ has been used to direct the assembly of spherical and tetrapod-shaped nanoparticles where the capillary force exerted by the edge of a drop of evaporating solvent confines particles against an edge,³⁹⁵ in a narrow channel,^{396–399} or in a well (Figure 15B).⁴⁰⁰

The assembly of nanowire arrays is more challenging than the assembly of nanoparticles and nanorods due to the anisotropic shape of the object.⁴⁰¹ Nano-

wire self-assembly results in partially ordered, small superlattices. This issue has been addressed by several new methods that direct the assembly of nanowires, including the use of microfluidic channels⁴⁰² and electric fields.⁴⁰³ Another review in this issue contains a full account of methods available to assemble nanowires.⁴⁰⁴

7.3. Summary

Self-assembly—as a stand-alone method for nanofabrication—is presently unable to produce structures with precise spatial positioning and arbitrary shapes with a low concentration of defects and functionality that can be achieved using conventional nanofabrication. It is also unable to generate the range of patterns required for even simple electronic functionality. Nontemplated self-assembly may represent a useful method of generating materials for information technology. For example, crystalline arrays of magnetic nanocrystals can store large amounts of information.³⁴⁸

Neither nontemplated nor templated self-assembly strategies have yet demonstrated a route to the level of functionality necessary to contribute to microelectronics other than by generating materials or positioning objects with large ($\sim 100 \mu\text{m}$) dimensions.^{197,405–409} We are, however, optimistic that self-assembly will play a significant role in nanofabrication in the future, particularly when we consider the potential for fabrication in three dimensions, the opportunity for reversible⁴¹⁰ and reconfigurable self-assembly,⁴¹¹ and the implication that self-assembled structures can undergo self-repair or self-replication.⁴¹² The cell is the ultimate demonstration and inspiration for continuing work in nanometer-scale self-assembly, and it is much *more* sophisticated than current microelectronic systems. A demonstration of the principle for *very* complex and functional forms of self-assembly thus already exists.

8. Outlook and Conclusions

The expectations surrounding nanotechnology and nanofabrication are high. Governments and companies around the world are spending billions of dollars on research related to nanotechnology. The U.S. government promoted an initiative announced in 2000 based on optimistic (probably *very* optimistic) predictions that it would eventually lead to “materials with ten times the strength of steel and only a fraction of the weight, the ability to shrink the information housed in the Library of Congress into a volume the size of a sugar cube and the ability to detect dangerous cancerous tumors when they are only a few cells in size”.¹⁷ This initiative predicts nanotechnology will add a trillion dollars to the gross national product and add 2 million new jobs to the U.S. economy by 2013. Whether nanotechnology will or will not have a revolutionary economic impact is not certain but also not highly relevant at this early, exploratory stage in its development. It is unquestionably fascinating science, and it will certainly lead to significant technologies.

Nanoscience and nanotechnology cover many different areas, but one key set of methods for both is

nanofabrication. The development of microelectronic circuits with <100-nm-scale features is proceeding rapidly by extensions of existing, conventional photolithographic techniques. Unconventional techniques and new materials will be required, if at all, for structures with dimensions below ~20 nm and to change the cost structure of this very capital-intensive industry. New products and technologies outside the field of microelectronics, but still requiring nanoscale fabrication, are being developed in widely different areas such as biology, materials science, and optics. Our intuition is that there will be many opportunities for the application of unconventional nanofabrication in these areas. Some possible areas of application include the following: (i) printed, low-cost organic microelectronics; (ii) sub-wavelength optics; (iii) tools for biology for investigating individual cells and cell-cell interactions; (iv) nanofluidics; (v) nanoelectrical mechanical systems (NEMS); and (vi) single-molecule studies.

The high capital and operating cost of conventional equipment for nanofabrication (photolithographic steppers and scanning beam tools) has given an opening for unconventional techniques in nanofabrication, initially in the areas of research and product development. Molding, embossing, and printing techniques have demonstrated usefulness, at least at the level of generating prototypes. A new generation of nanofabrication tools is emerging—mainly from small businesses located in the United States, Europe, and Asia—that enable various forms of stamping, molding, and embossing. These products vary in sophistication and capability. Most of the tools that are being sold are going to research laboratories or to fabrication facilities for products other than semiconductor-based devices. The nanofabrication equipment manufacturers are selling tools to companies that are reportedly developing a variety of single-layer products such as surface acoustic wave devices, photonic crystals, microfluidics, and biosensors. We expect that unconventional nanofabrication will play an important role in the commercialization of nanotechnology-based devices that require low-cost manufacturing of nanoscale structures over large areas, on nonplanar surfaces, and with new materials.

Historically, the motivation for developing unconventional nanofabrication techniques was to continue patterning semiconductor devices beyond the predicted resolution limit of photolithography—to forestall an end to “Moore’s Law”.⁴¹³ Photolithography continues to overcome obstacles to achieve new resolution requirements.¹ These improvements have not always been simple. This challenge is reflected in the increasing costs of conventional photolithography processes when shifting to shorter imaging wavelengths, improved imaging optics, phase-shifting masks, and optical proximity correction. Complex engineering challenges remain. It is, however, now clear that it is not feature sizes (or even registration) that is the limitation in microelectronic device performance. The major challenges are connections within or from the chip, power distribution, and heat dissipation. These areas may offer opportunities for unconventional nanofabrication.

The intrinsic limits of photolithography are inescapably related to the diffraction of light. The attraction of many unconventional nanofabrication techniques is that their intrinsic limitations are *not* set by optics and diffraction but by van der Waals interactions and the granularity of matter at the molecular or atomic scale. The resolution of molding methods in the laboratory is currently ~1 nm,^{163,169} but extensive development will be required to achieve <10-nm feature sizes reliably in manufacturing. Unconventional nanofabrication methods may, however, offer higher resolution patterning at a lower cost than photolithography. Conventional scanning beam lithography also has high initial equipment costs. Scanning beam lithography is, however, required by many nanofabrication techniques to pattern the master or mask. The major advantage most unconventional methods have over conventional scanning beam techniques is the high throughput in replicating the original pattern.

Each technique for nanofabrication has certain characteristic advantages, and it is unlikely that a single technique will dominate all areas of application. For example, molding, embossing, or printing from a cylindrical drum^{121,184} or by wave printing^{414,415} or perhaps self-assembly have the potential to generate regular patterns over very large areas inexpensively. Photolithography will, however, probably remain the exclusive method for producing high-performance integrated circuits for years to come.

Regardless of their ultimate commercial applications, unconventional techniques for nanofabrication provide uncomplicated methods for the research communities in a broad range of disciplines to explore nanoscience and begin to develop nanotechnology. Their variety, low cost, operational simplicity, and broad collective applicability open the door to surveying and exploring many areas to which high-resolution photolithography and particle-beam writing simply are not applicable. Unconventional techniques for nanofabrication are being widely accepted and developed in this exploratory spirit and are thus a key part of the development of nanoscale research and development. From them may come unexpected inventions and technologies that will allow nanotechnology to live up to the optimistic expectations for its future.

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